




  
BLKCDN.100/VH/TM1SQ/W.100/1


  
BLKCDN.100/VH/TM2DE/W.200/2


  
BLKCDN.100/RH/TM2DE/W.525/2


  
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BLKCDN.100/VH/TM1SQ/W.100/2


  
BLKCDN.100/VH/TM1SQS/W.100/2


  
BLKCDN.100/RH/TM1SQS/W.350/2


  
BLKCDN.100/RH/TM2DE/W.525/4


  
BLKCDN.100/VH/TM2DE/W.200/4


  
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BLKCDN.100/RH/TM2DE/W.52


  
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BLKCDN.100/VH/TM1SQ/W.100/3


  
BLKCDN.100/RH/TM1SQS/W.350/3


  
BLKCDN.100/VH/TM2DE/W.200/6


  
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BLKCDN.100/RH/TM2DE/W.525/8


  
BLKCDN.100/VH/TM1SQ/W.10


  
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
  
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BLKCDN.100/RH/TM1SQ/W.425/5


  
BLKCDN.100/VH/TM1SQ/W.100/5

  
BLKCDN.100/VH/TM1SQS/W.100/5

  
BLKCDN.100/RH/TM1SQS/W.350/5


  
BLKCDN.100/RH/TM2DE/W.525/10


  
BLKCDN.100/RH/TM1SQ/W.425/6


  
BLKCDN.100/VH/TM2DE/W.200/10


  
BLKCDN.100/VH/TM1SQS/W.100/6


  
BLKCDN.100/VH/TM1SQ/W.100/6

  
BLKCDN.100/RH/TM1SQS/W.350/6


  
BLKCDN.100/RH/TM2DE/W.525/12


  
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BLKCDN.100/VH/TM1SQS/W.100/7

  
BLKCDN.100/RH/TM1SQS/W.350/7


  
BLKCDN.100/RH/TM2DE/W.525/14


  
BLKCDN.100/RH/TM1SQ/W.425/8


  
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BLKCDN.100/VH/TM1SQ/W.100/8

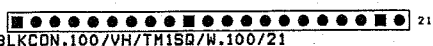
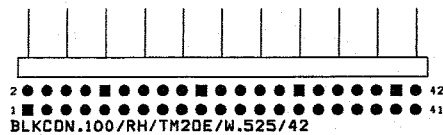
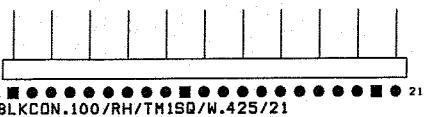
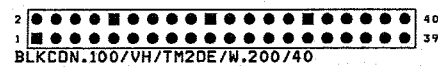
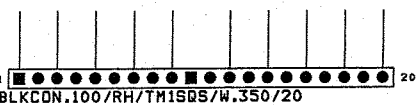
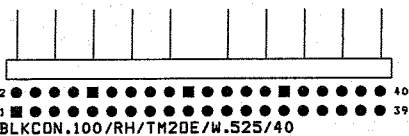
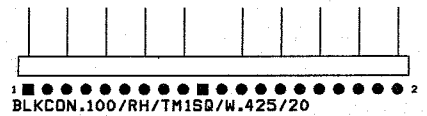
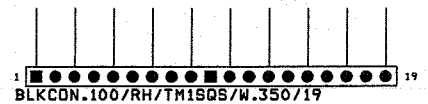
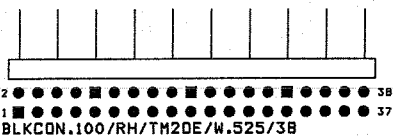
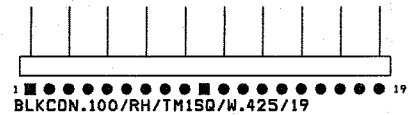
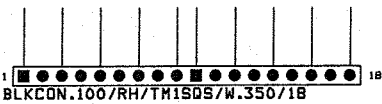
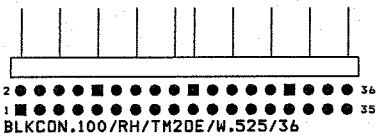
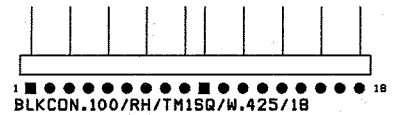
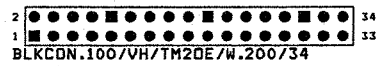
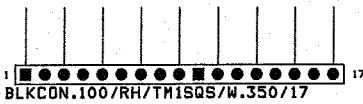
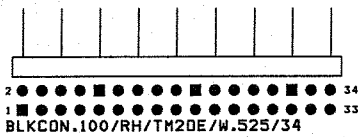
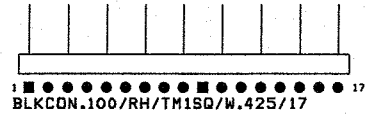
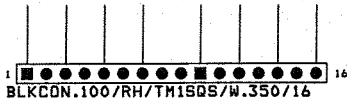
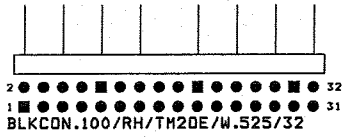
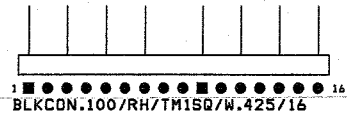
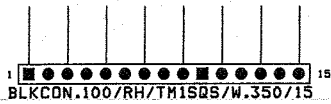
  
BLKCDN.100/VH/TM1SQS/W.100/8

  
BLKCDN.100/RH/TM1SQS/W.350/8

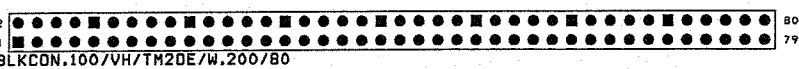
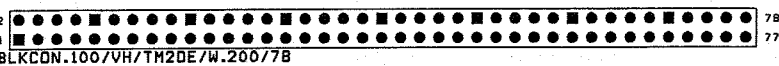
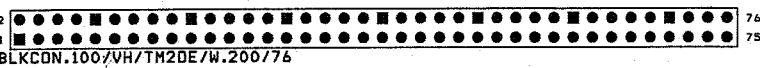
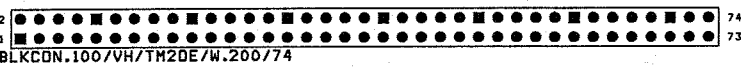
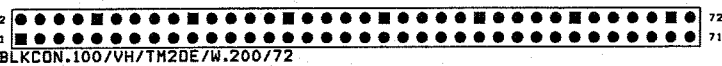
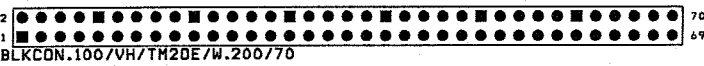
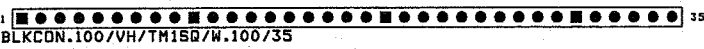
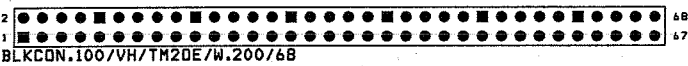
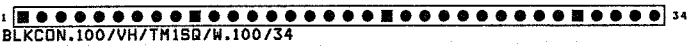
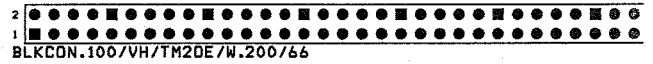
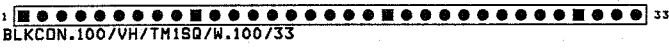
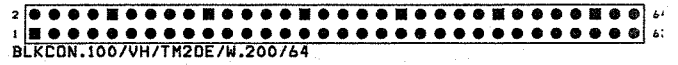
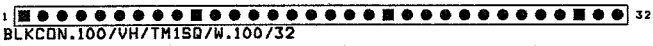
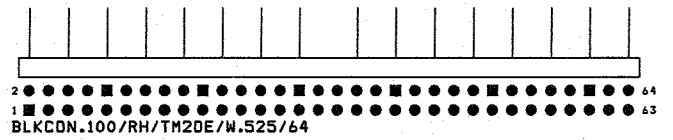
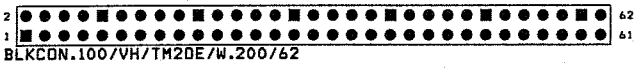
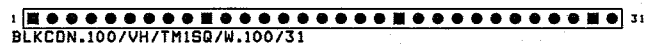
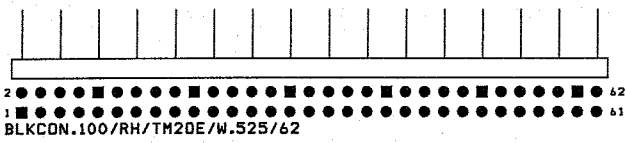
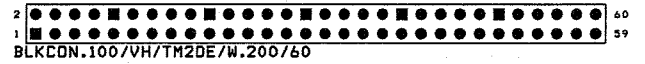
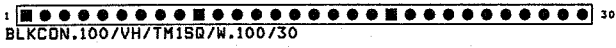
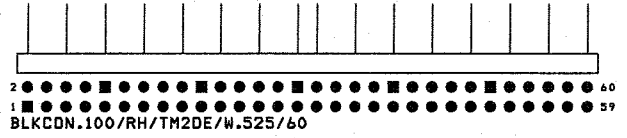
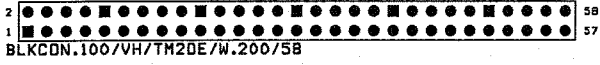
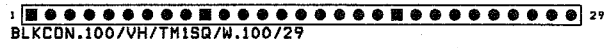
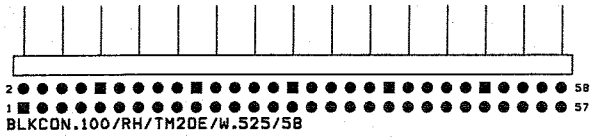
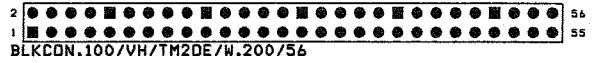
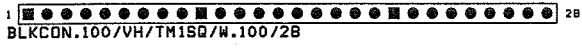
  
BLKCDN.100/RH/TM2DE/W.525/16

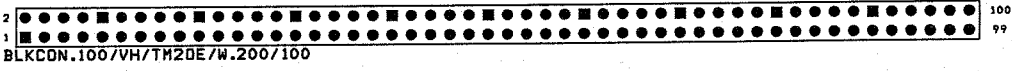
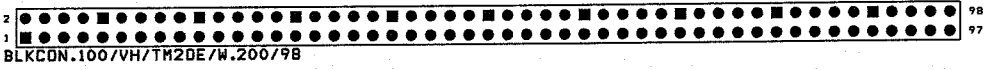
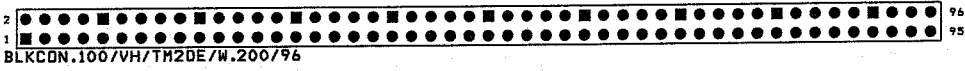
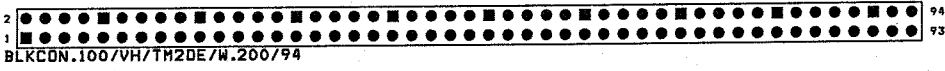
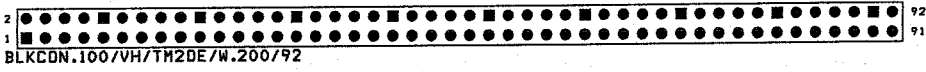
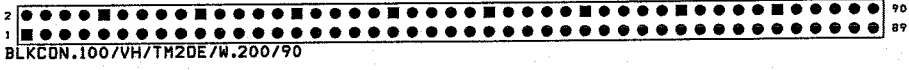
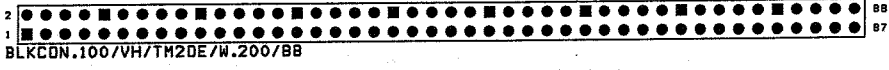
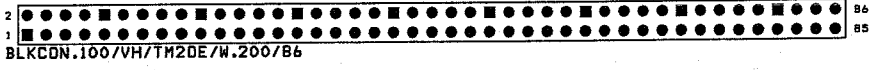
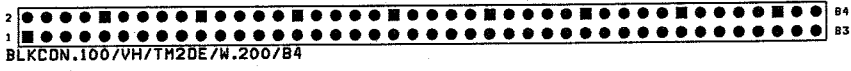
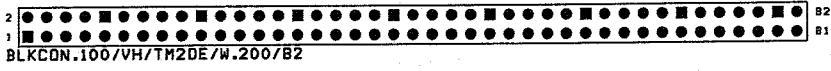
  
BLKCDN.100/RH/TM1SQ/W.425/9













DIP.100/6/W.300/L.375



DIP.100/8/W.300/L.400



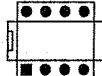
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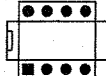
DIP.100/12/W.300/L.450



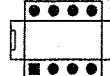
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DIP.100/16/W.300/L.500



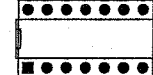
DIP.100/18/W.300/L.525



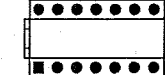
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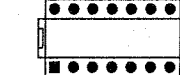
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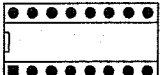
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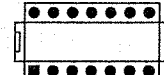
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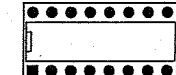
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DIP.100/30/W.300/L.800



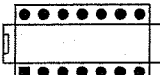
DIP.100/32/W.300/L.800



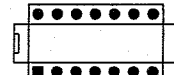
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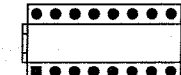
DIP.100/36/W.300/L.825



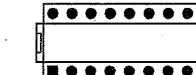
DIP.100/38/W.300/L.825



DIP.100/40/W.300/L.850



DIP.100/42/W.300/L.950



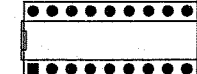
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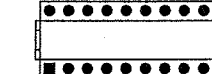
DIP.100/46/W.300/L.900



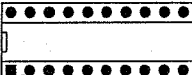
DIP.100/48/W.300/L.900



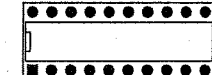
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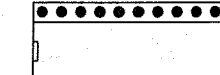
DIP.100/52/W.300/L.950



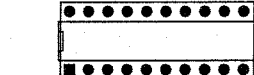
DIP.100/54/W.300/L.1.000



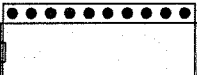
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DIP.100/58/W.400/L.1.000



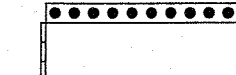
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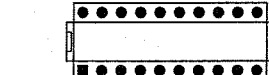
DIP.100/62/W.300/L.1.025



DIP.100/64/W.300/L.1.050



DIP.100/66/W.400/L.1.050



DIP.100/68/W.300/L.1.075



DIP.100/70/W.300/L.1.100



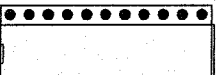
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DIP.100/74/W.300/L.1.075



DIP.100/76/W.300/L.1.125



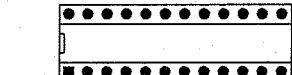
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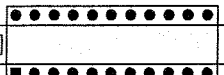
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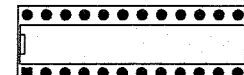
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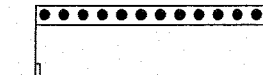
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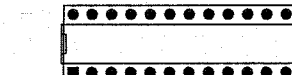
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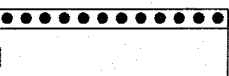
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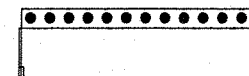
DIP.100/90/W.600/L.1.200



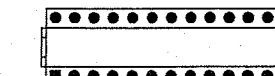
DIP.100/92/W.300/L.1.225



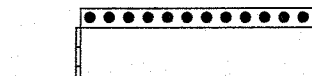
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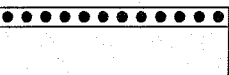
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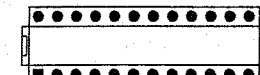
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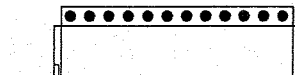
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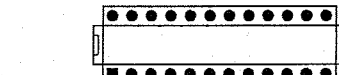
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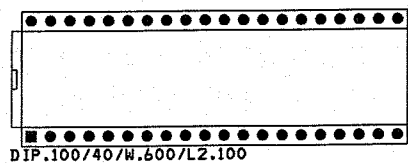
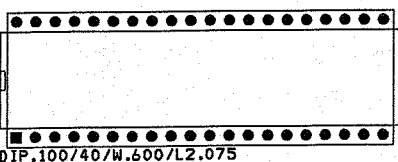
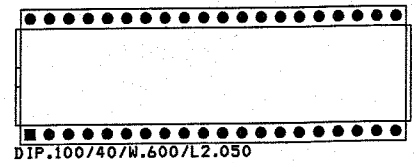
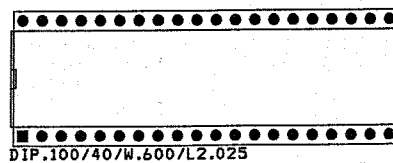
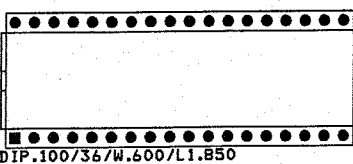
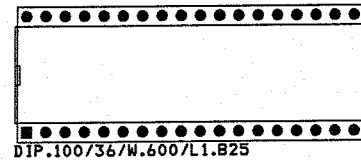
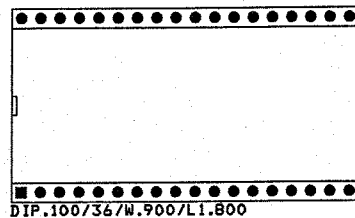
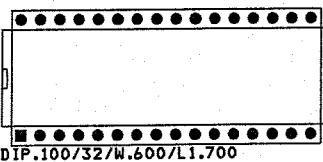
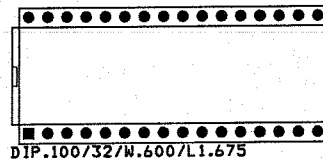
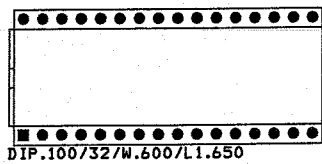
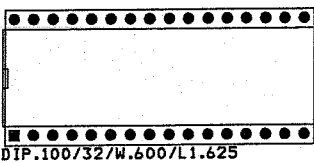
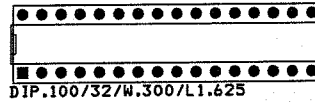
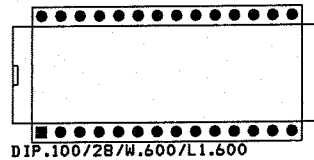
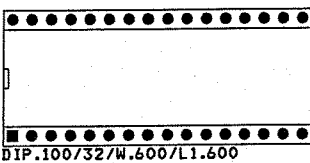
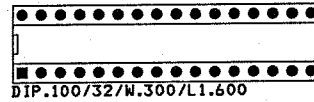
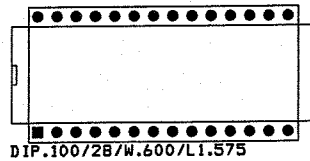
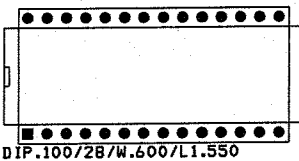
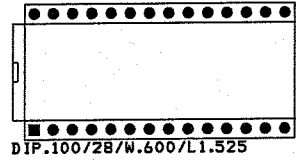
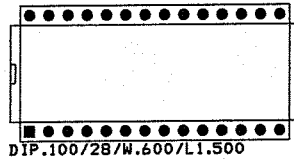
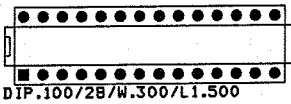
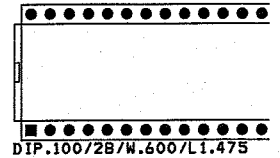
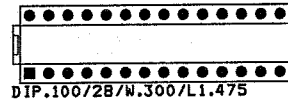
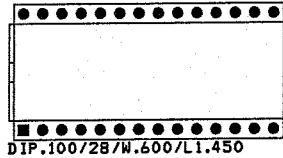
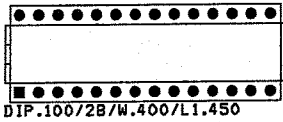
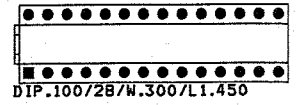
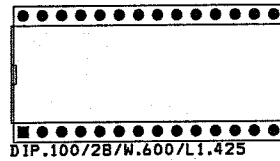
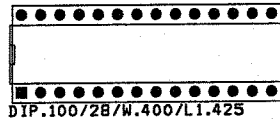
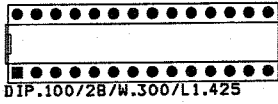
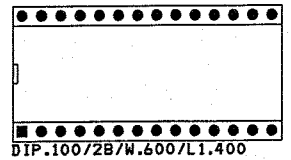
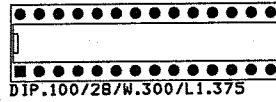
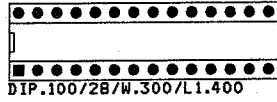
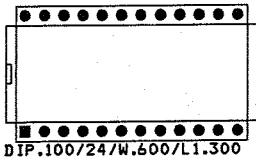
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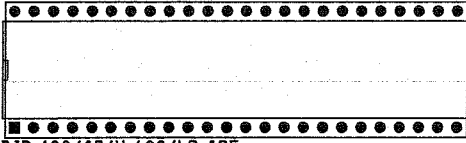
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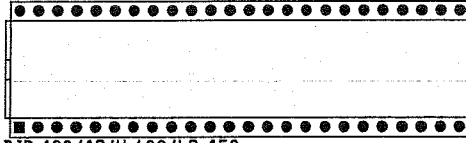
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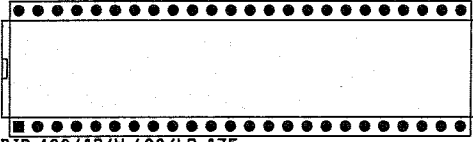




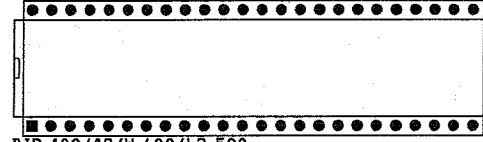
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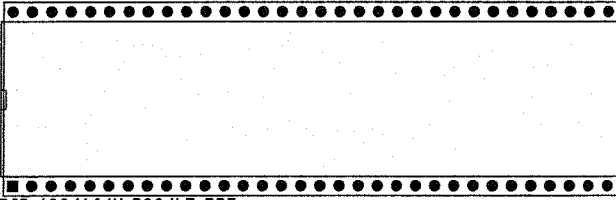
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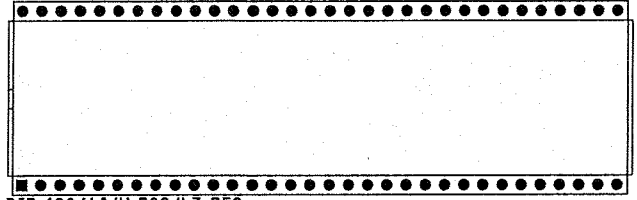
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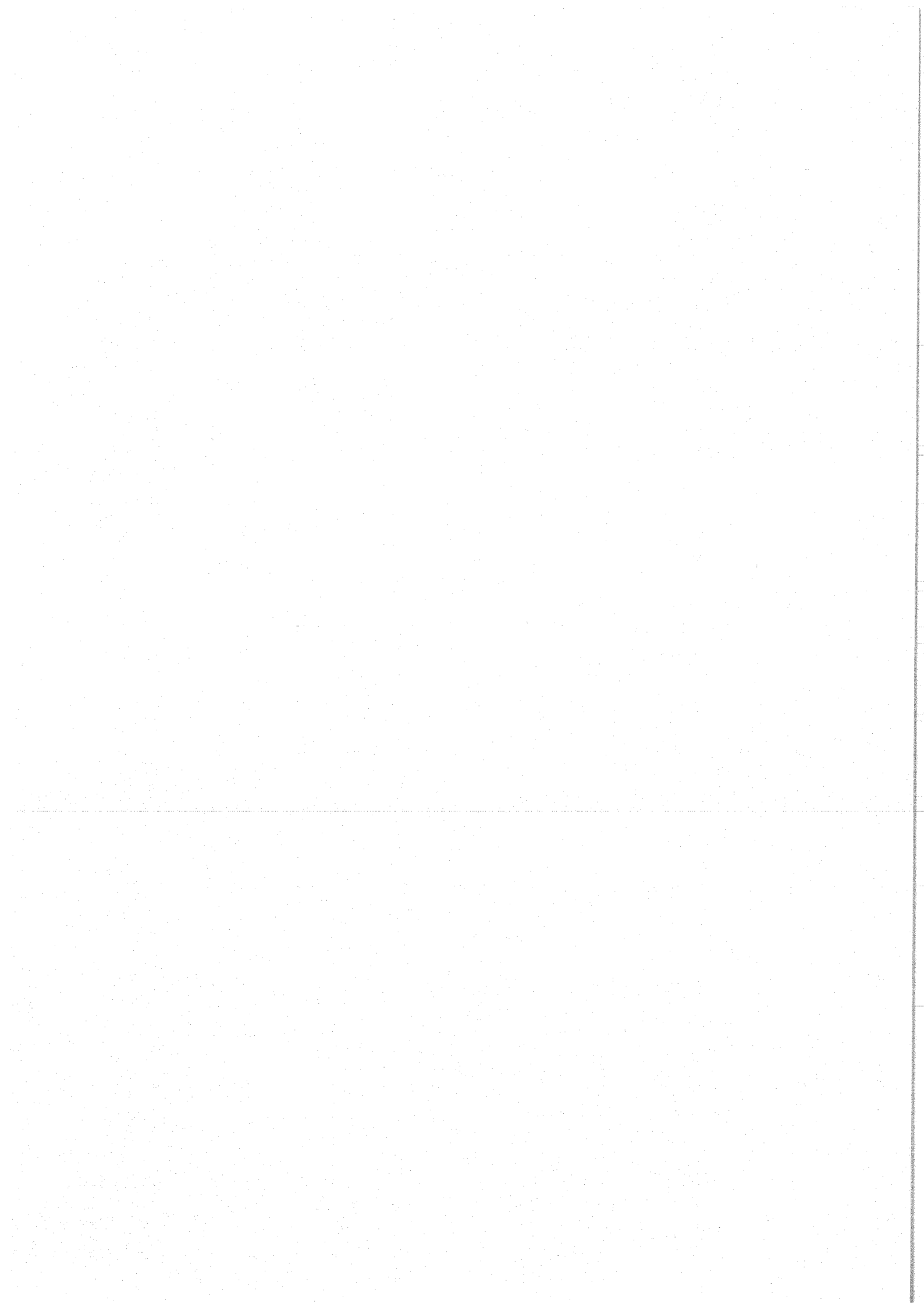
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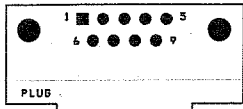


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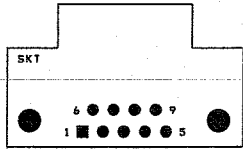


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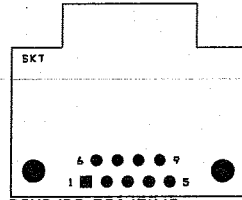




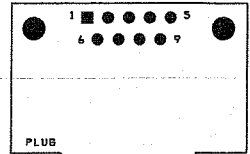
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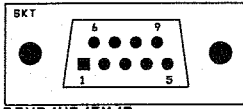
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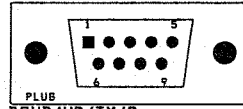
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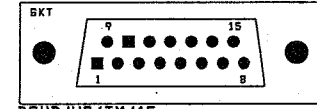
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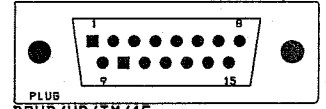
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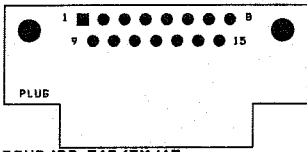
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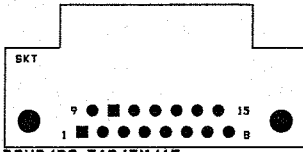
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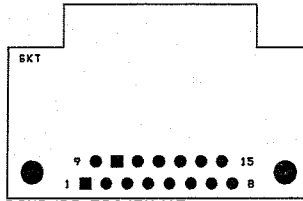
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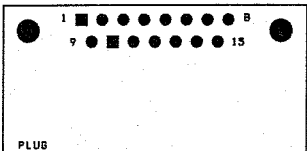
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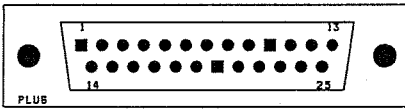
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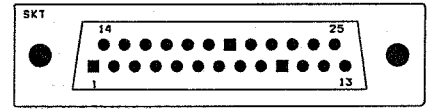
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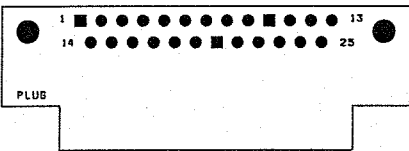
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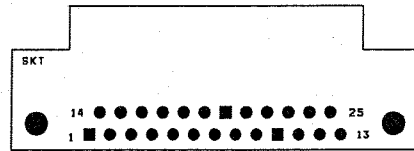
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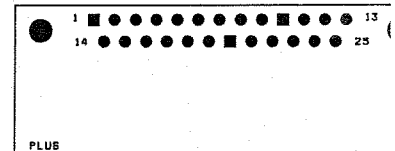
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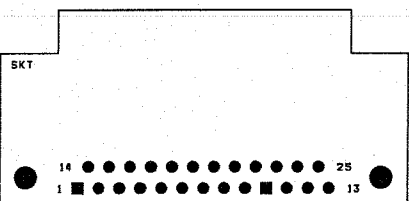
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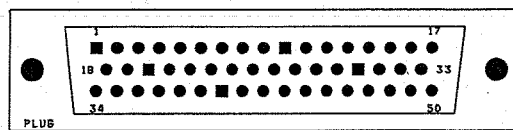
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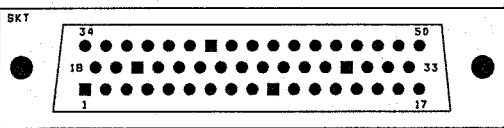
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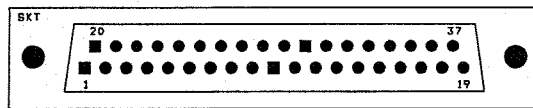
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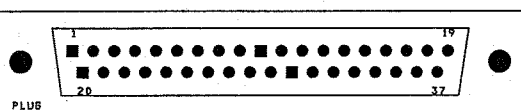
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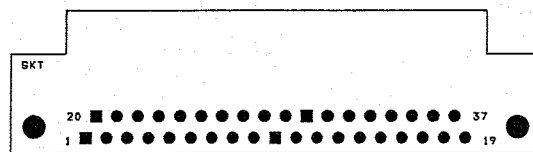
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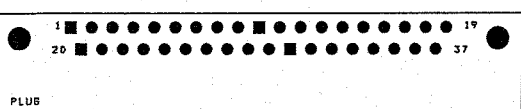
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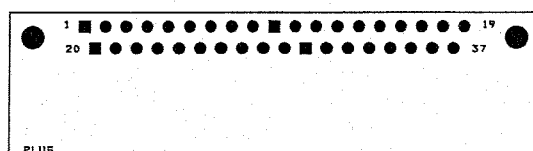
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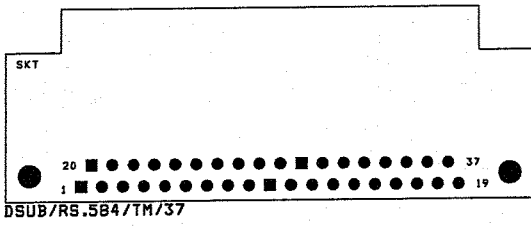
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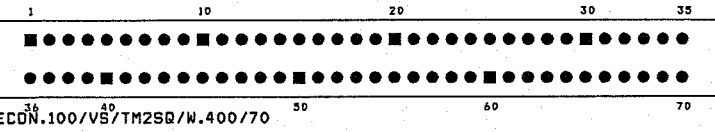
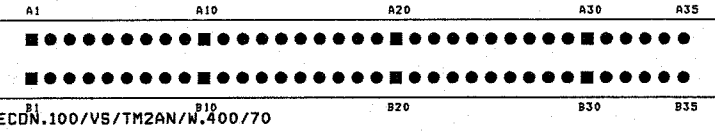
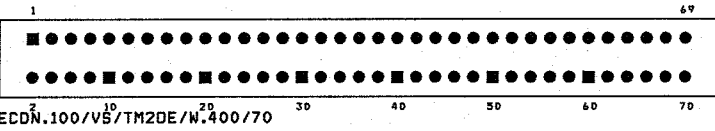
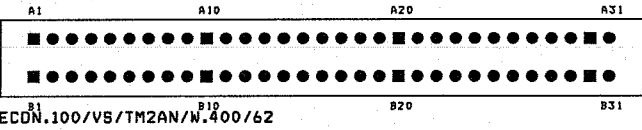
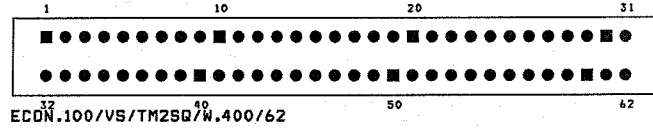
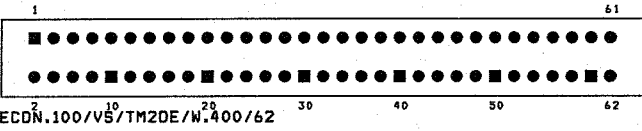
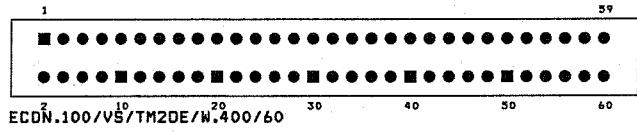
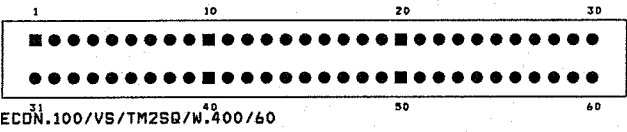
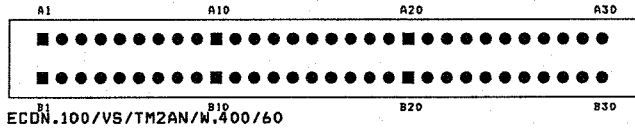
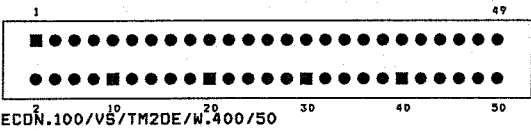
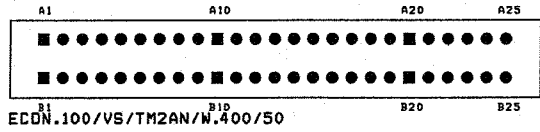
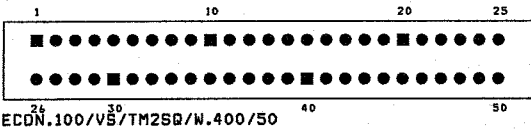
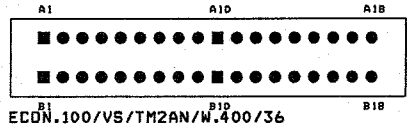
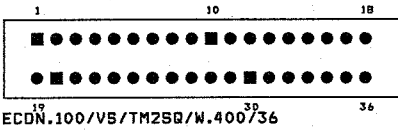
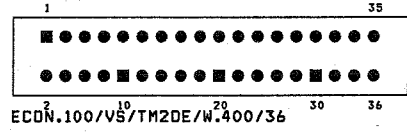
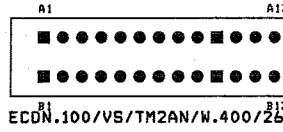
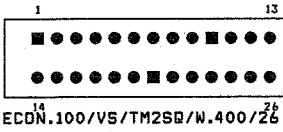
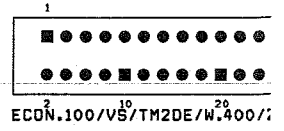
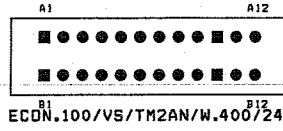
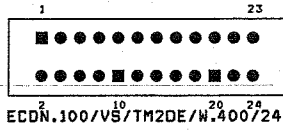
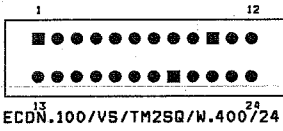


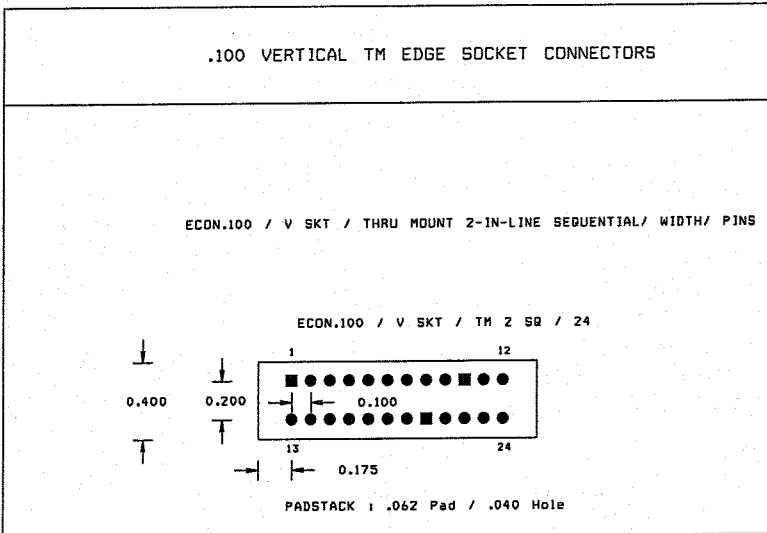
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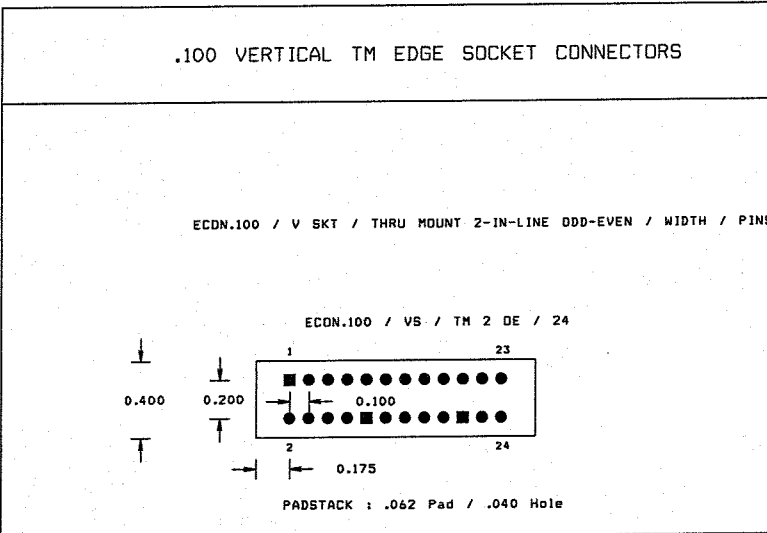
DSUB/RP.584/TM/37



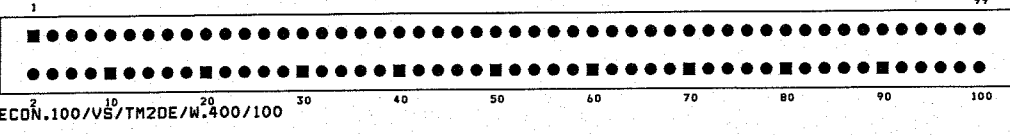
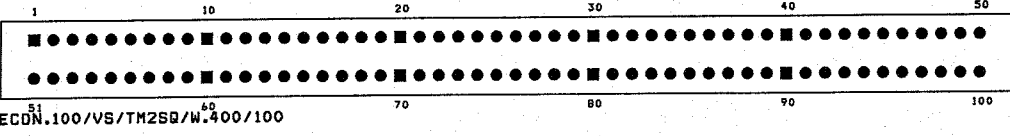
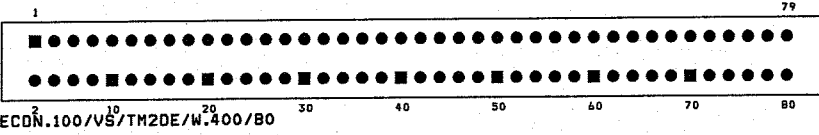
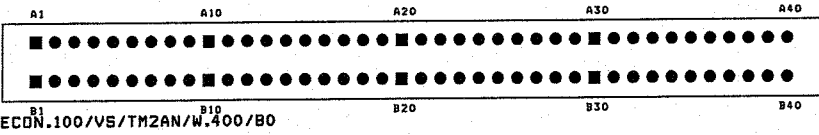
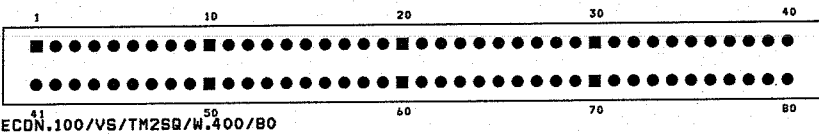


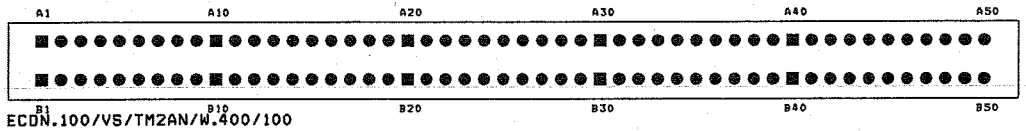


ECON.100/VS/TM2SR/W.400/#\_HELP

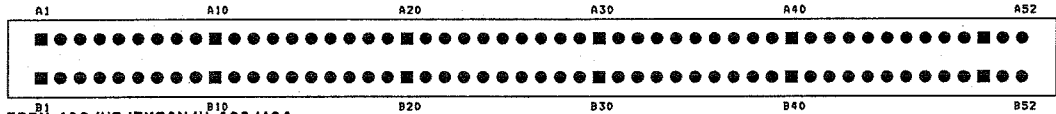


ECON.100/VS/TM2DE/W.400/#\_HELP

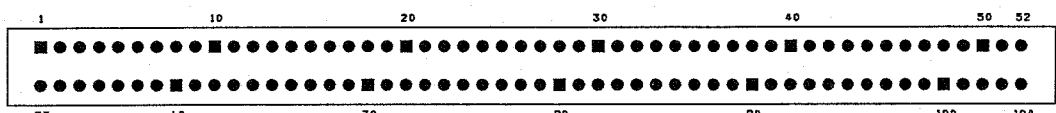




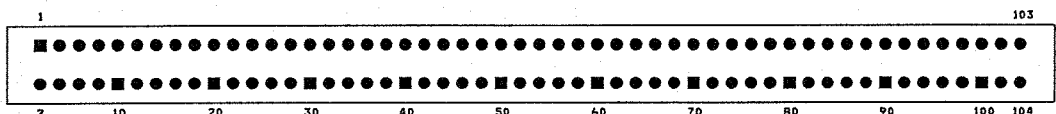
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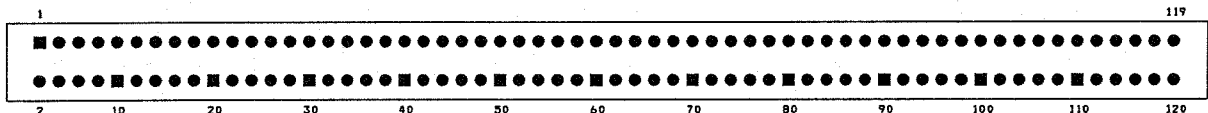
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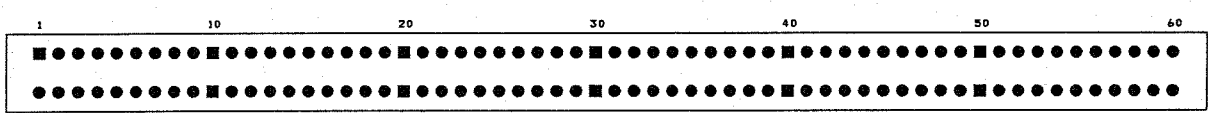
ECON.100/VS/TM2SQ/W.400/104



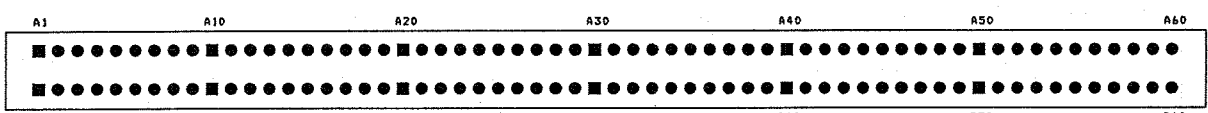
ECON.100/VS/TM2DE/W.400/104



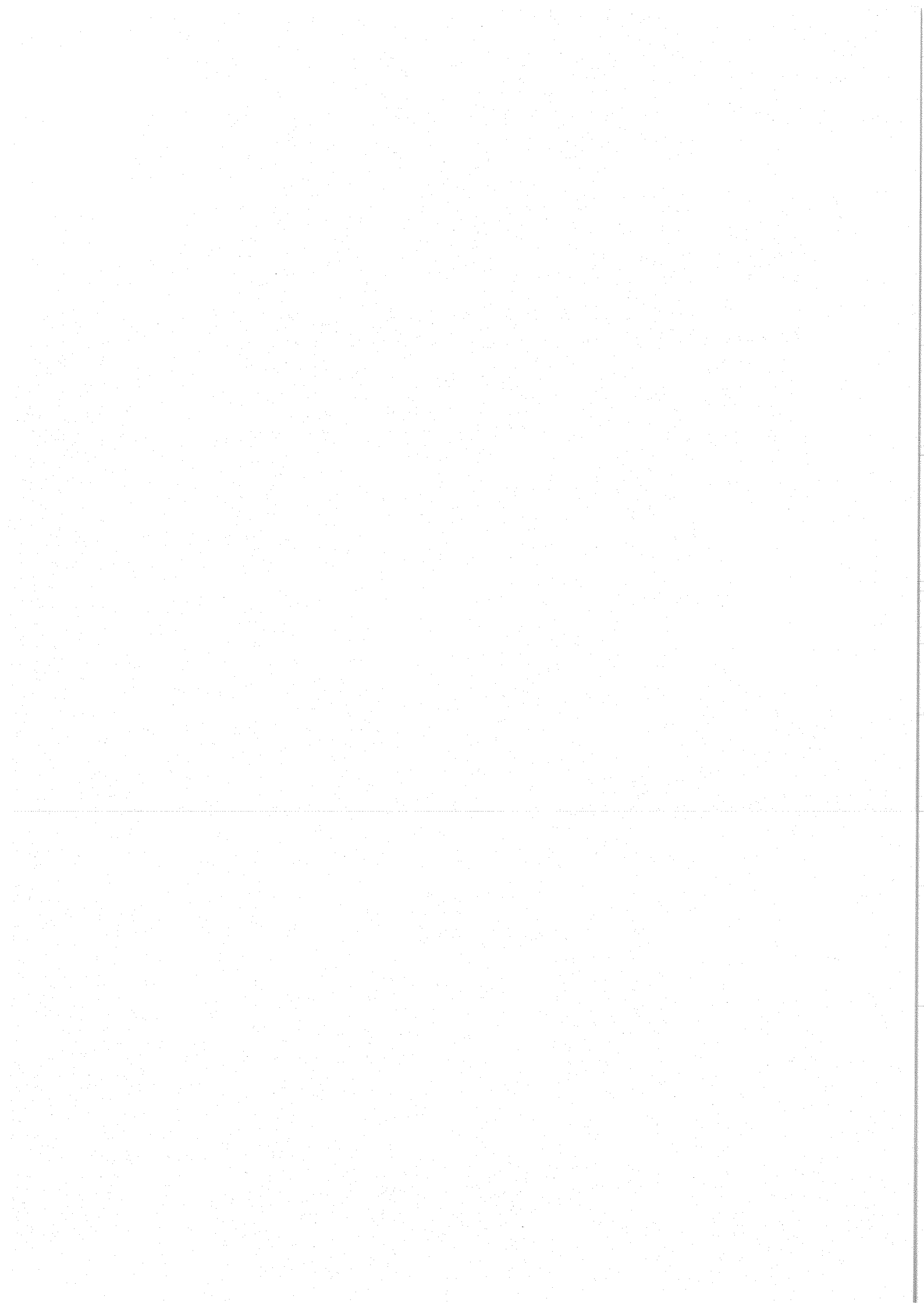
ECON.100/VS/TM2DE/W.400/120



ECON.100/VS/TM2SQ/W.400/120



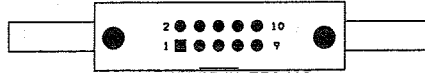
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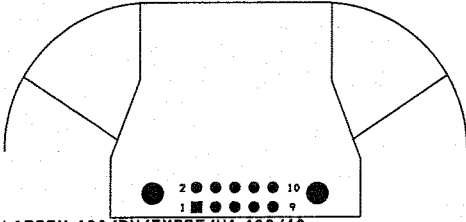




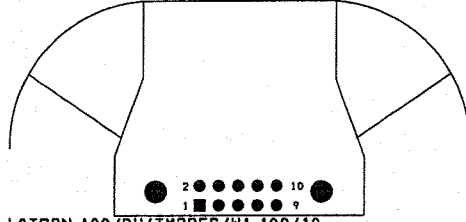
LATCDN.100/VH/TM2DE/W.350/10



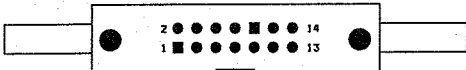
LATCDN.100/VH/TM2DES/W.350/10



LATCDN.100/RH/TM2DE/W1.100/10



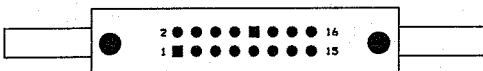
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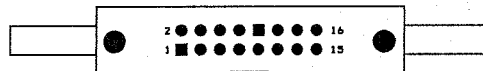
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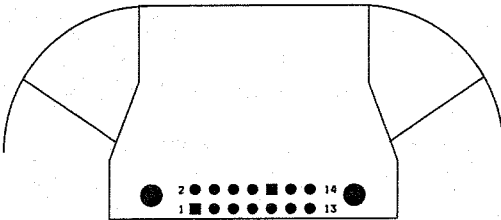
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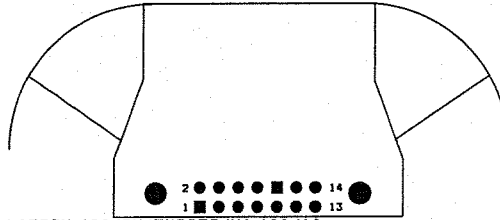
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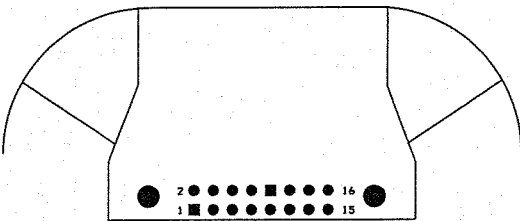
LATCDN.100/VH/TM2DES/W.350/16



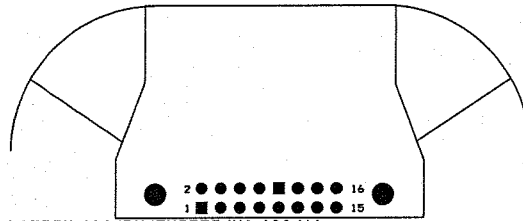
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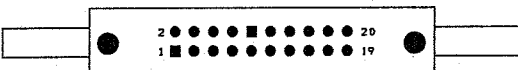
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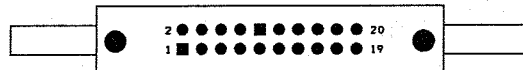
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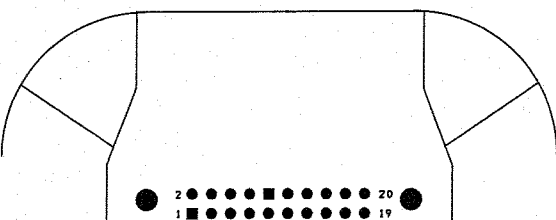
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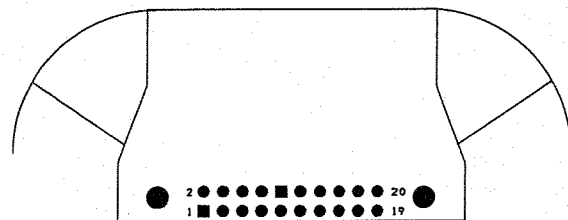
LATCDN.100/VH/TM2DE/W.350/20



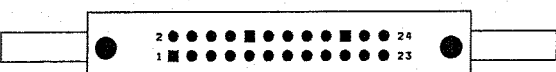
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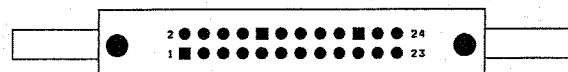
LATCDN.100/RH/TM2DE/W1.100/20



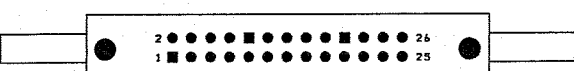
LATCDN.100/RH/TM2DES/W1.100/20



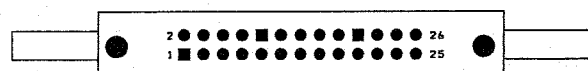
LATCDN.100/VH/TM2DE/W.350/24



LATCDN.100/VH/TM2DES/W.350/24

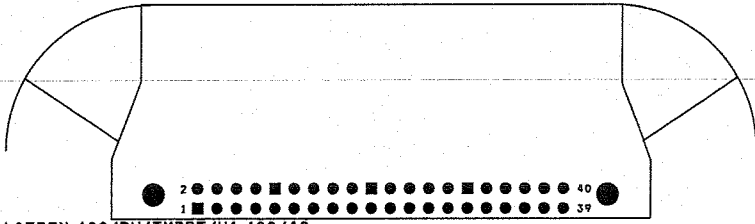


LATCDN.100/VH/TM2DE/W.350/26

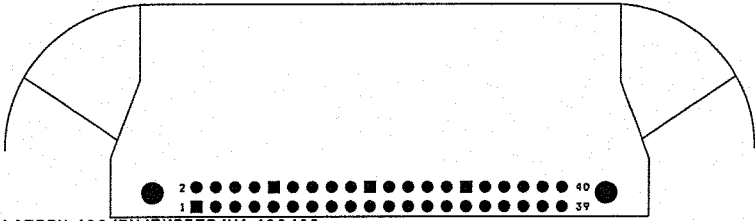


LATCDN.100/VH/TM2DES/W.350/26

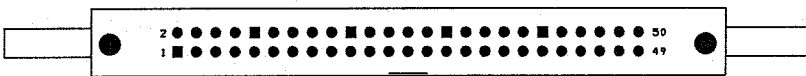




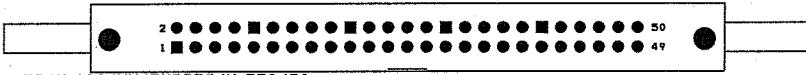
LATCON.100/RH/TM2DE/W1.100/40



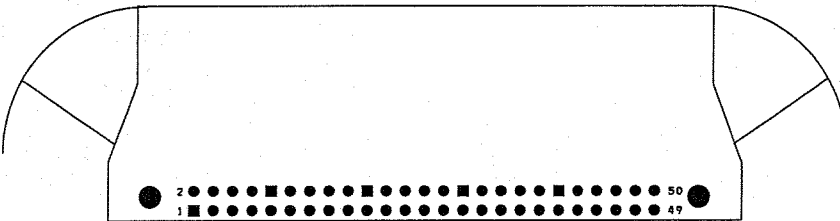
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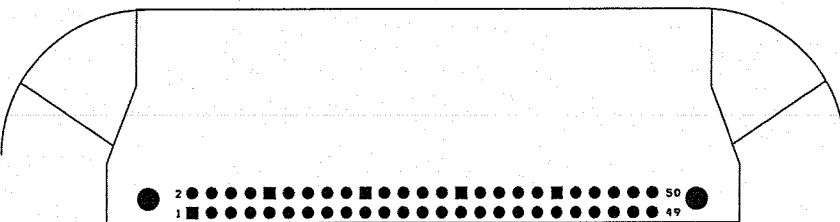
LATCON.100/VH/TM2DE/W.350/50



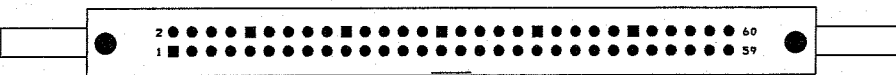
LATCON.100/VH/TM2DES/W.350/50



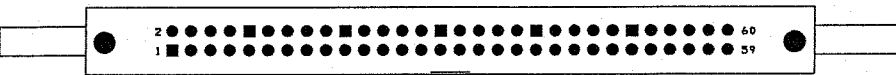
LATCON.100/RH/TM2DE/W1.100/50



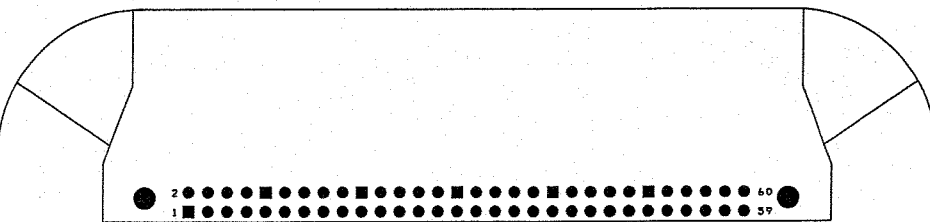
LATCON.100/RH/TM2DES/W1.100/50



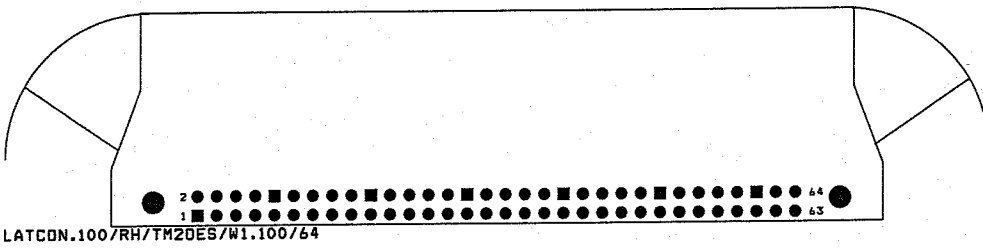
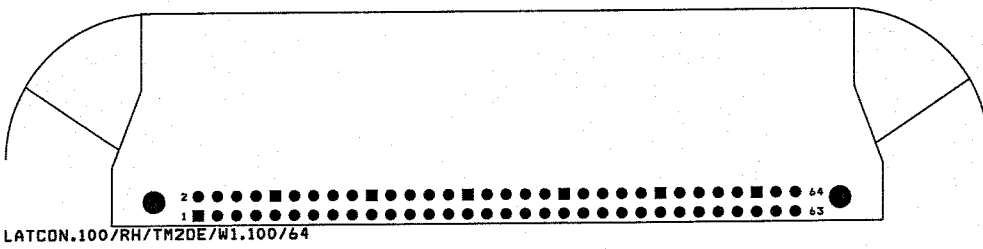
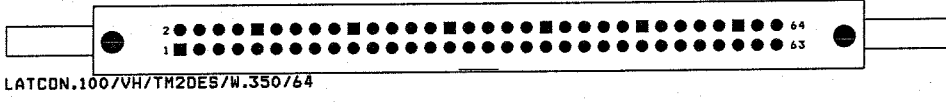
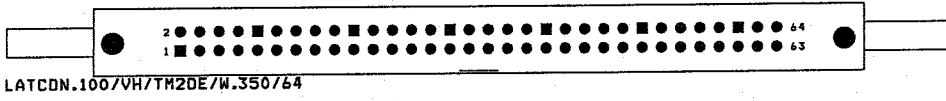
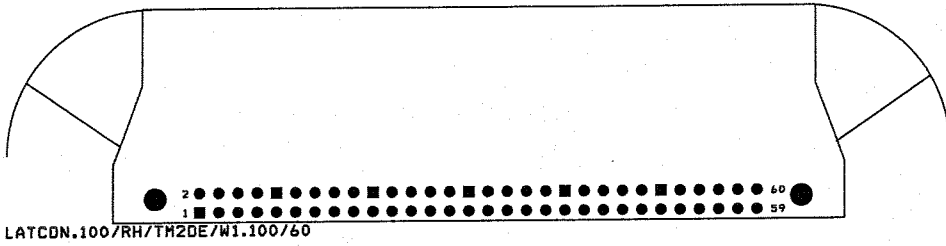
LATCON.100/VH/TM2DE/W.350/60



LATCON.100/VH/TM2DES/W.350/60



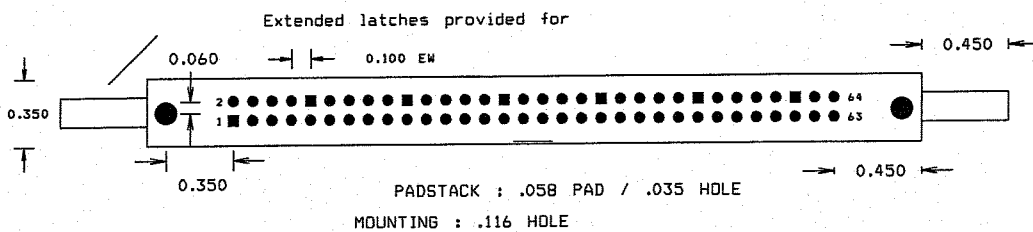
LATCON.100/RH/TM2DES/W1.100/60



.100 VERTICAL TM LATCH CONNECTORS

LATCDN.100/ V HDR/ THRU MOUNT 2-IN-LINE ODD-EVEN/ WIDTH/ PINS

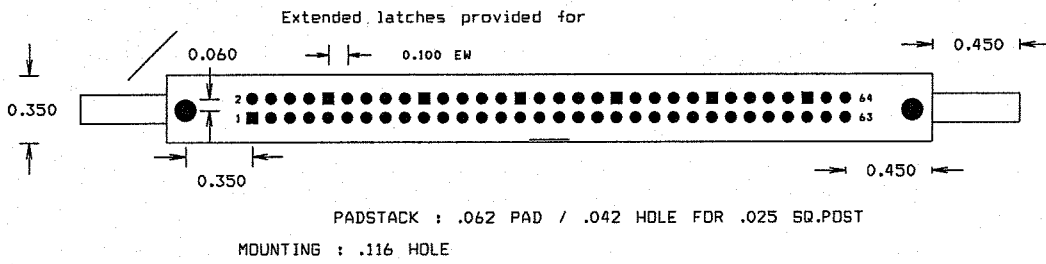
LATCDN.100/ V HDR / TM 2 DE / W .350 / 64



.100 VERTICAL TM LATCH HEADERS

LATCDN.100/ V HDR/ THRU MDUNT 2-IN-LINE ODD-EVEN SQ/ WIDTH/ PIN

LATCDN.100 / V HDR / TM 2 DE S / W .350 / 64

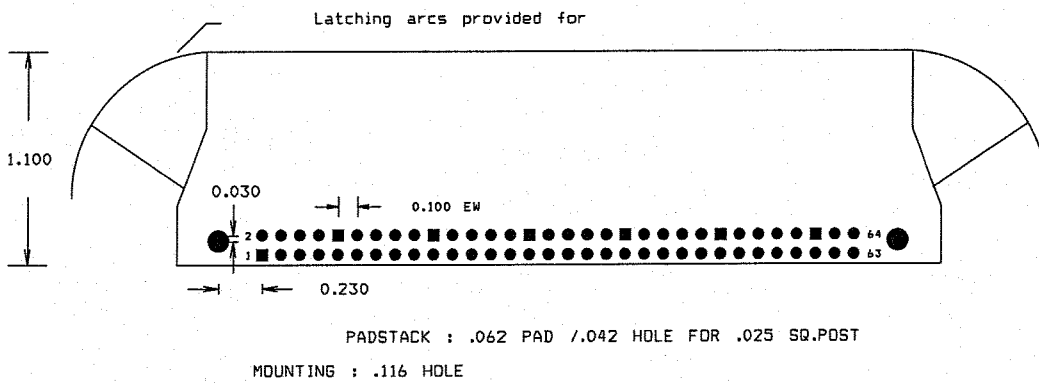


LATCDN.100/VH/TM2DES/W.350/#\_HELP

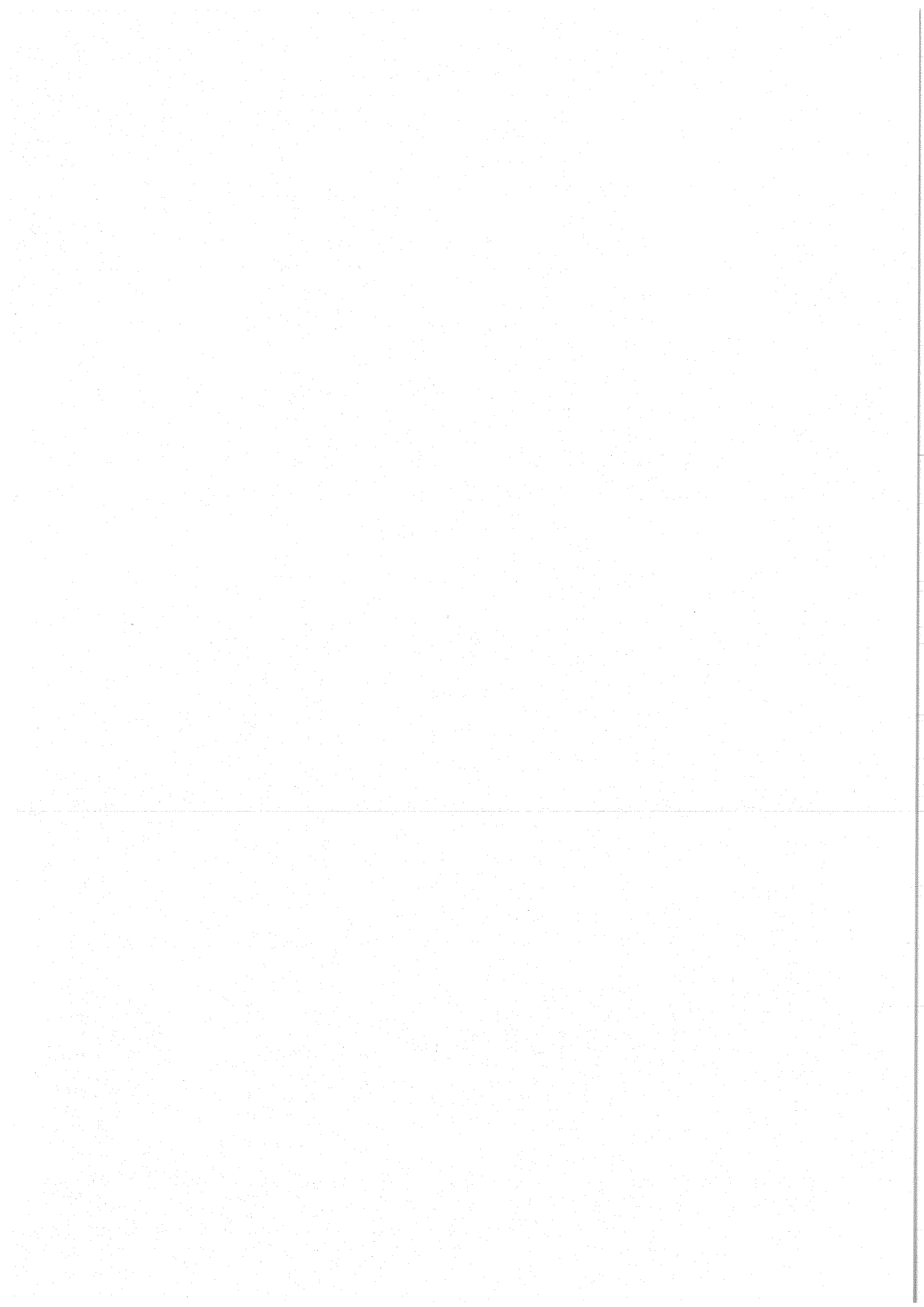
.100 RT.ANGLE TM LATCH CONNECTORS

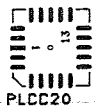
LATCDN.100/ R HDR/ THRU MDUNT 2-IN-LINE ODD-EVEN SQ/ WIDTH/ PIN

LATCDN.100/ RH / TM 2 DE S / W1.100 / 64



LATCDN.100/RH/TM2DES/W1.100/#\_HELP

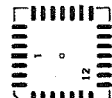




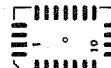
PLCC20



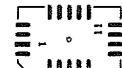
PLCC18/.244X.384



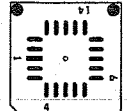
PLCC28



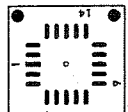
PLCC22/.244X.444



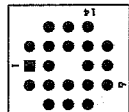
PLCC18/.244X.444



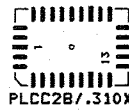
PLCC20/SMS/.575



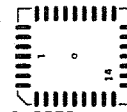
PLCC20/SMS/.600



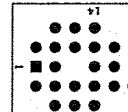
PLCC20/TMS/.625



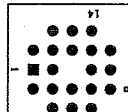
PLCC28/.310X.510



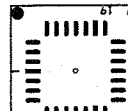
PLCC32



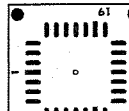
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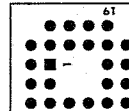
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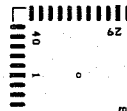
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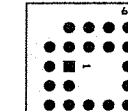
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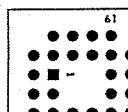
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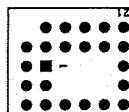
PLCC44



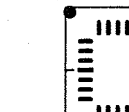
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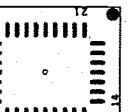
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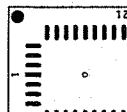
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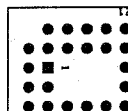
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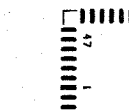
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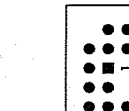
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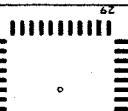
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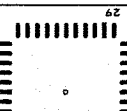
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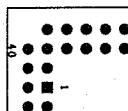
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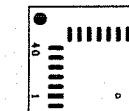
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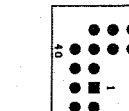
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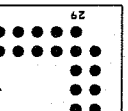
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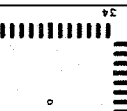
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PLCC44/TMS/.950



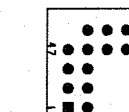
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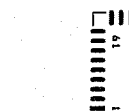
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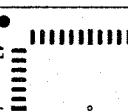
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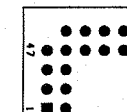
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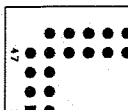
PLCC68



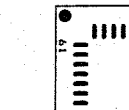
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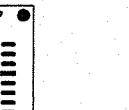
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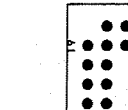
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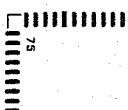
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PLCC68/SMS/1.225



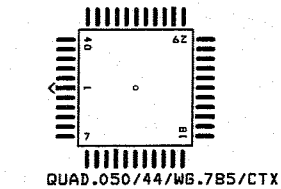
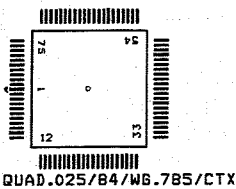
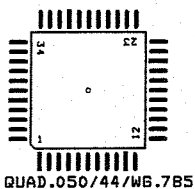
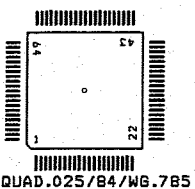
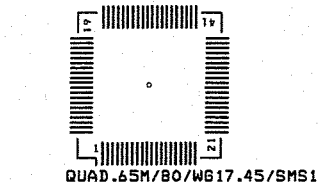
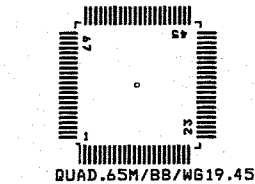
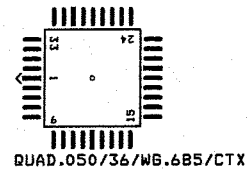
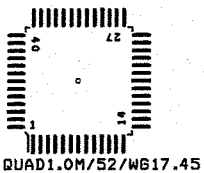
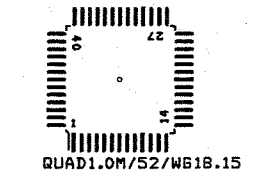
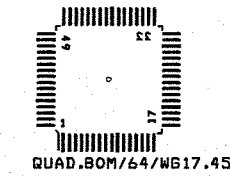
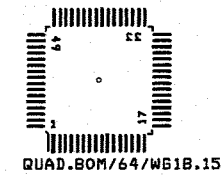
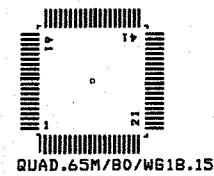
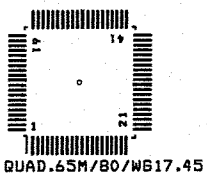
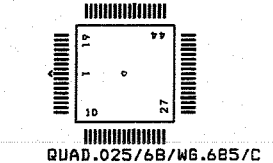
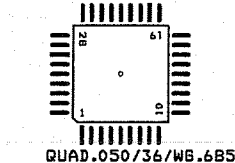
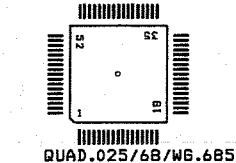
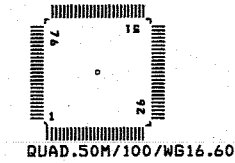
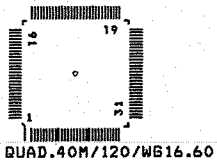
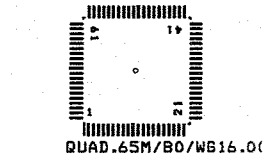
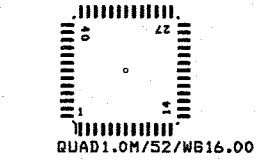
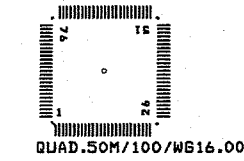
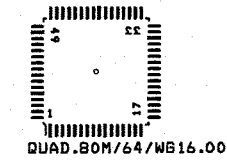
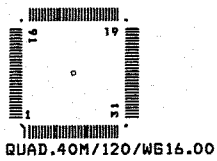
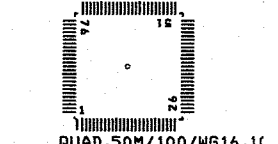
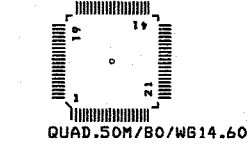
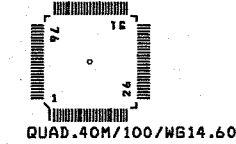
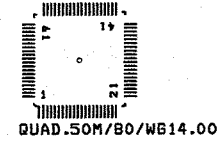
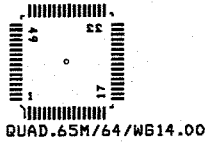
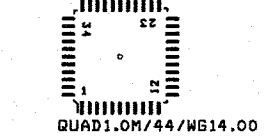
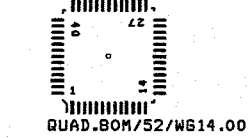
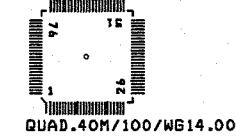
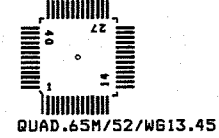
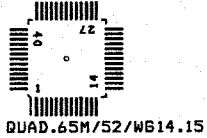
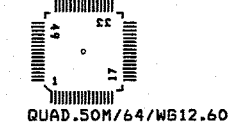
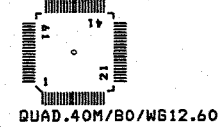
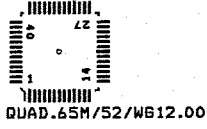
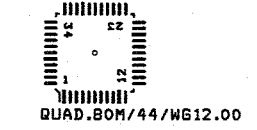
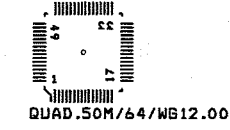
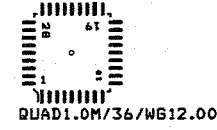
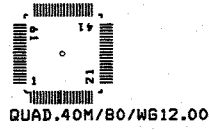
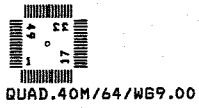
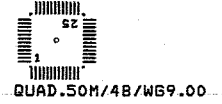
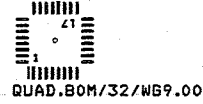
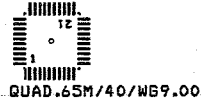
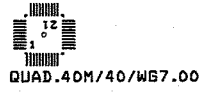
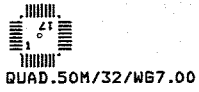
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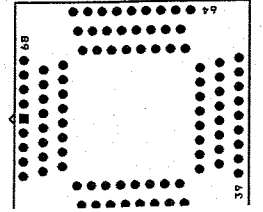
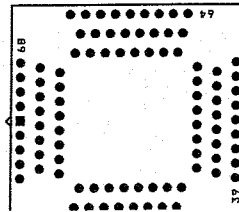
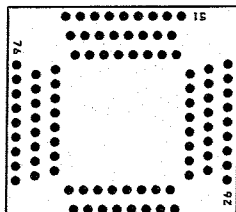
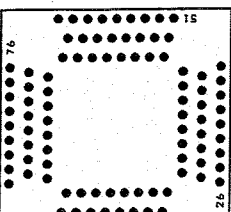
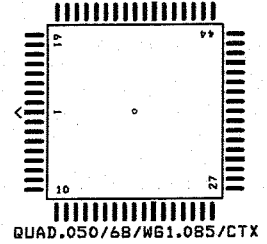
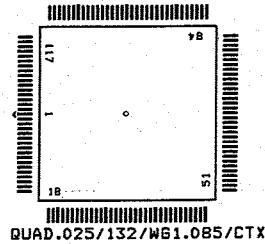
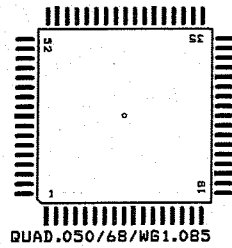
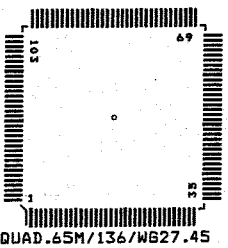
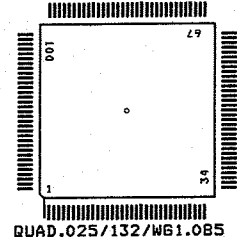
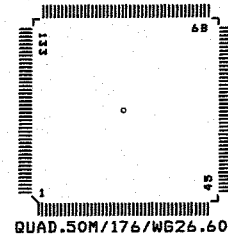
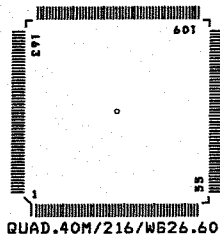
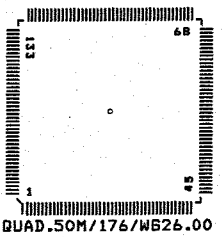
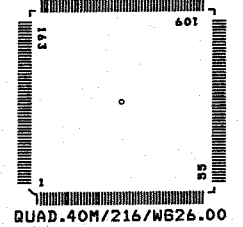
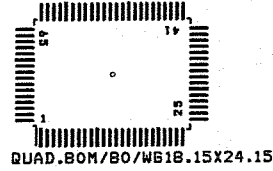
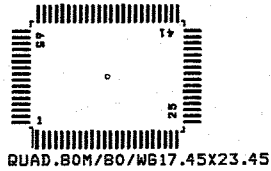
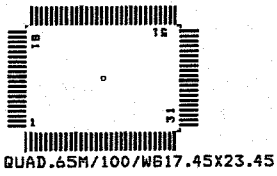
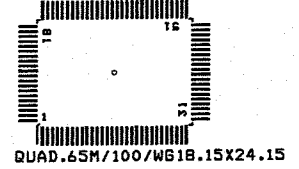
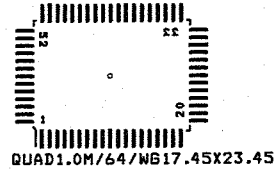
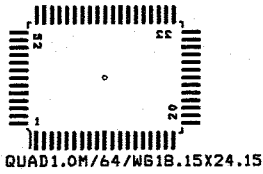
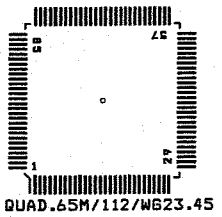
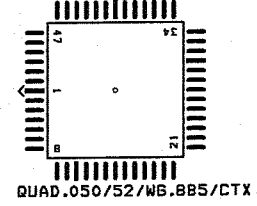
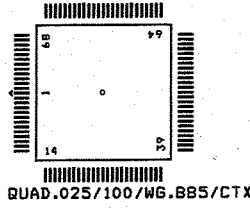
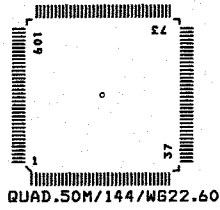
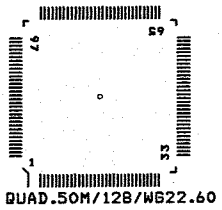
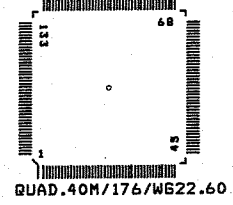
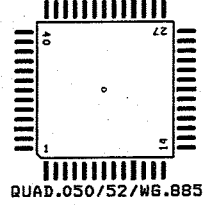
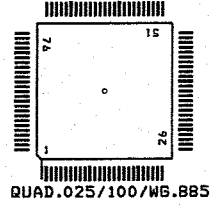
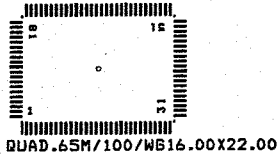
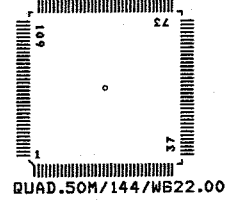
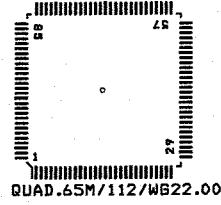
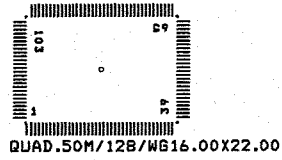
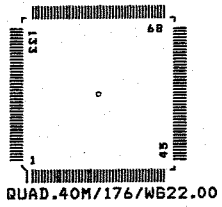


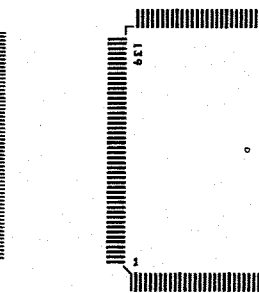
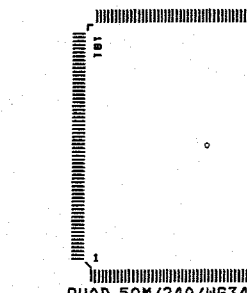
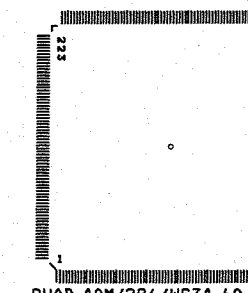
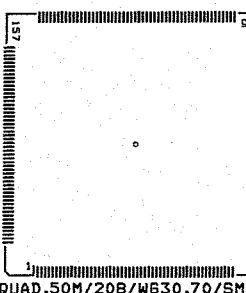
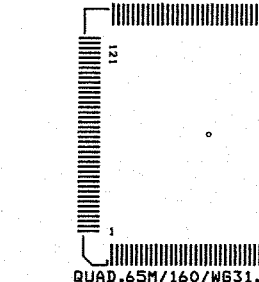
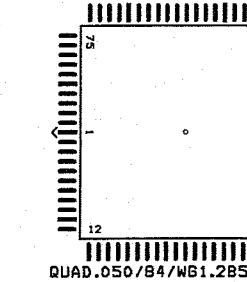
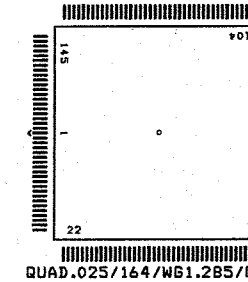
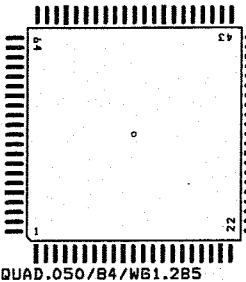
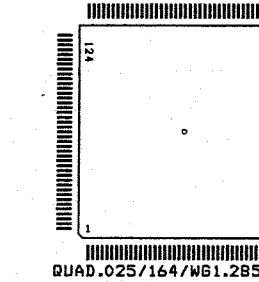
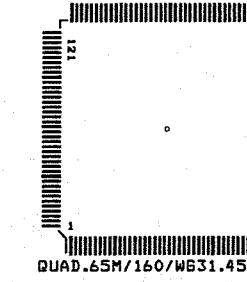
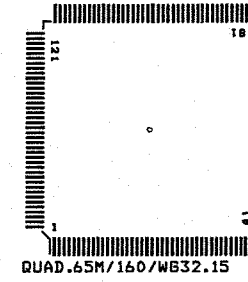
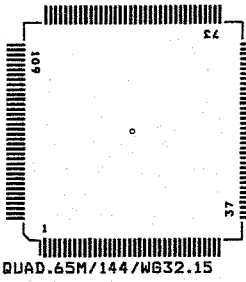
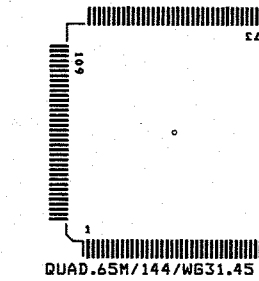
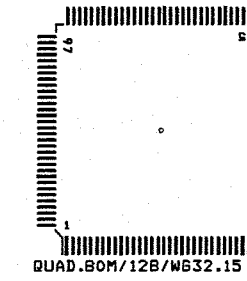
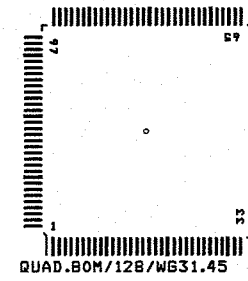
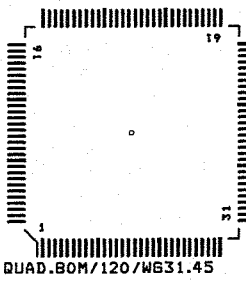
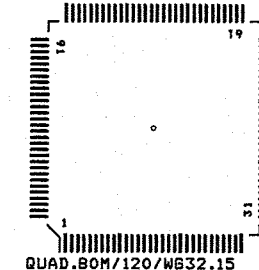
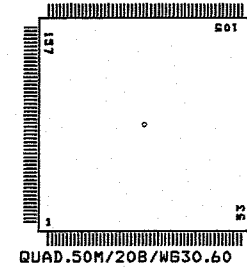
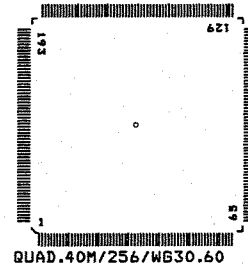
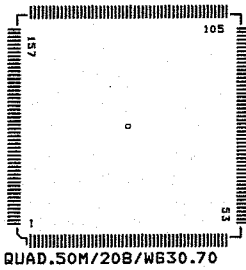
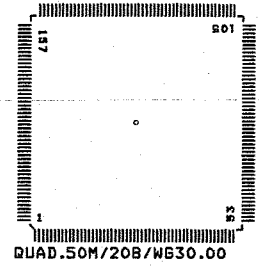
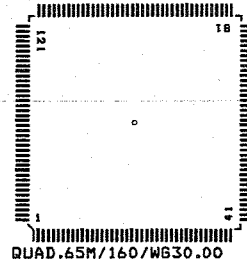
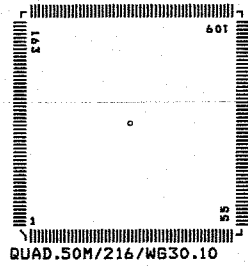
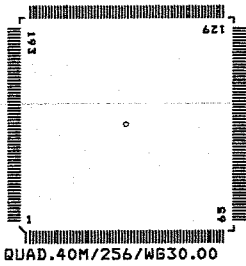
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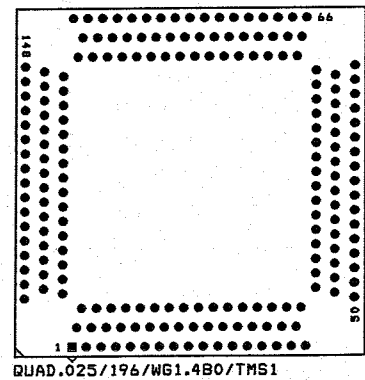
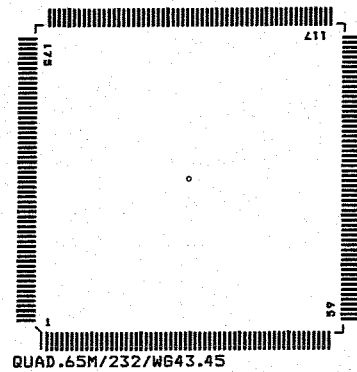
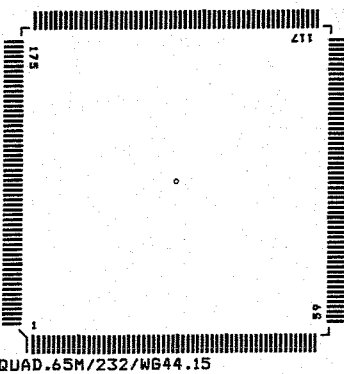
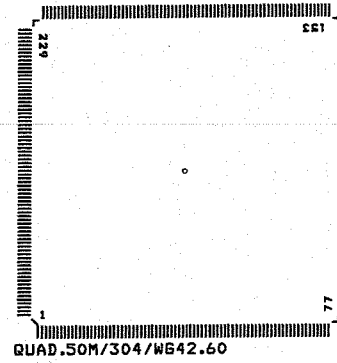
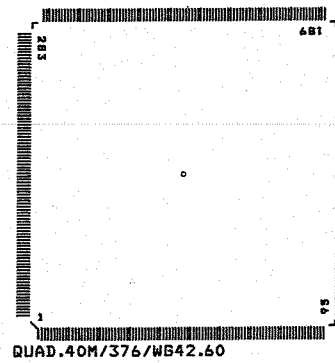
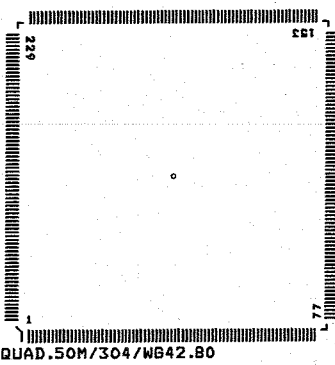
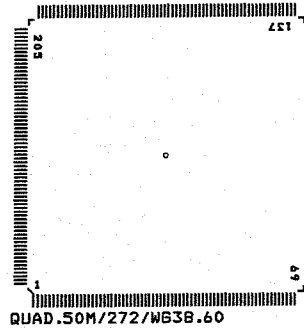
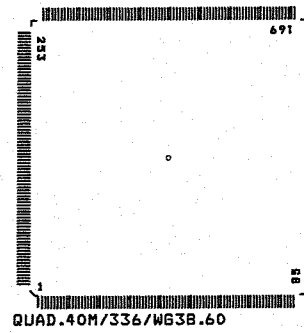
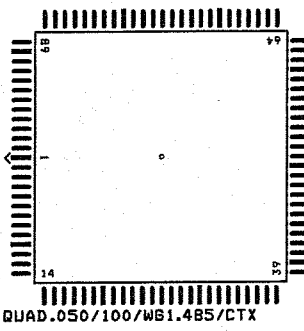
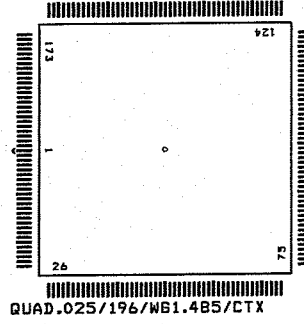
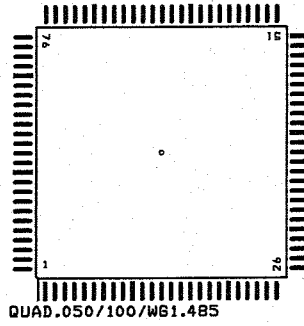
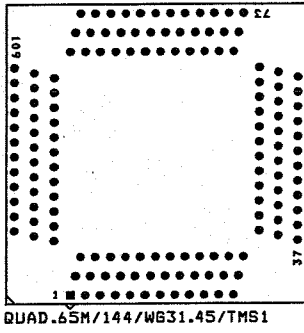
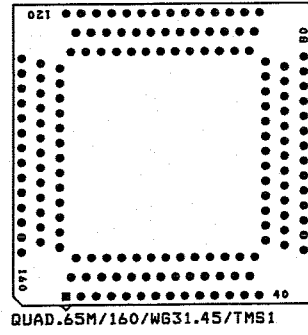
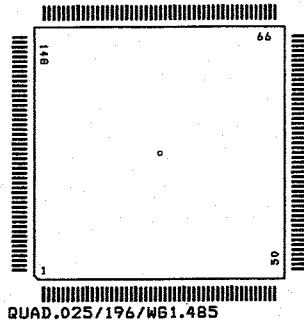
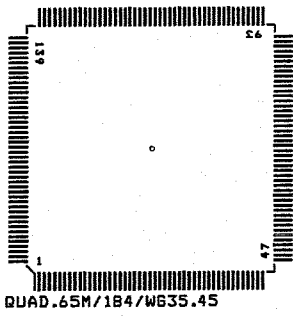


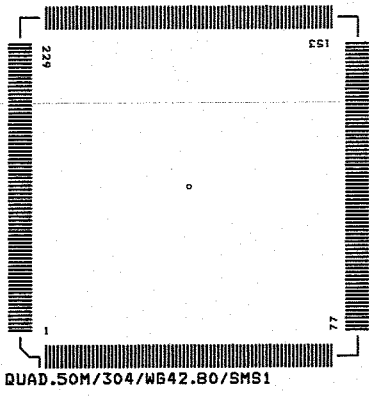


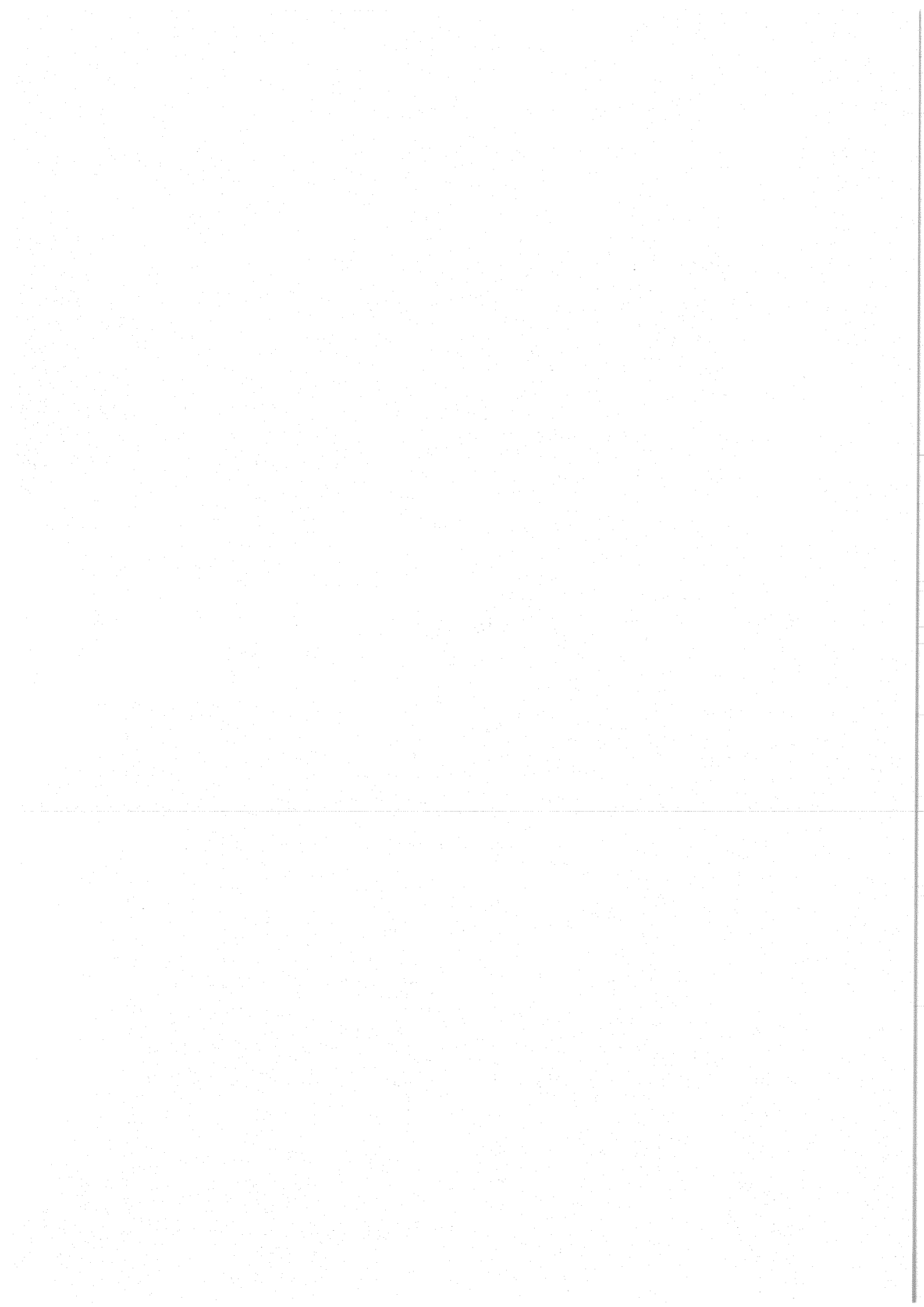


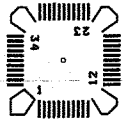




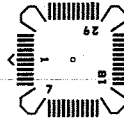




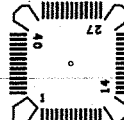




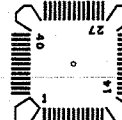
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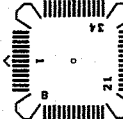
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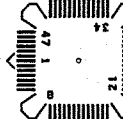
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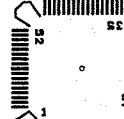
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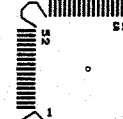
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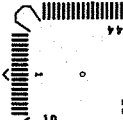
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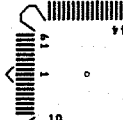
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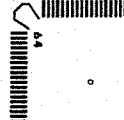
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QUADB.025/68/W6.685/CTX



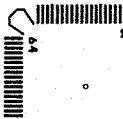
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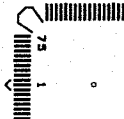
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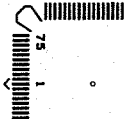
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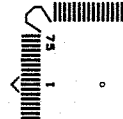
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QUADB.025/84/W6.785/CTX



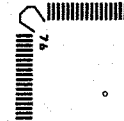
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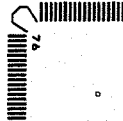
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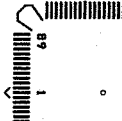
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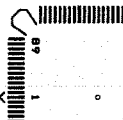
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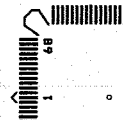
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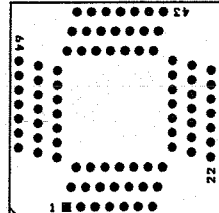
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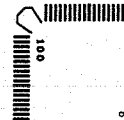
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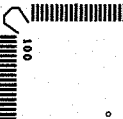
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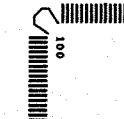
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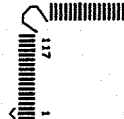
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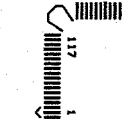
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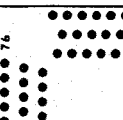
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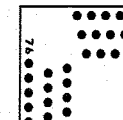
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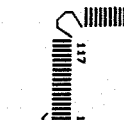
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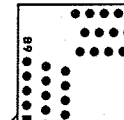
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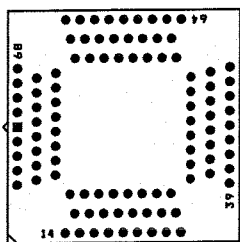
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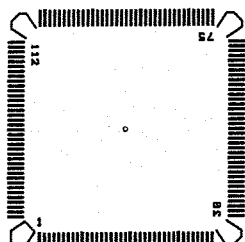
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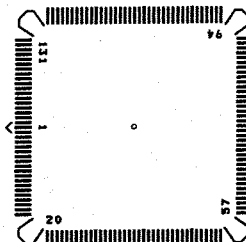
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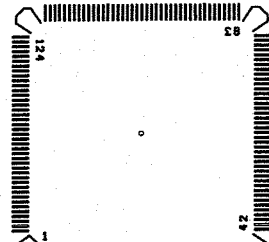
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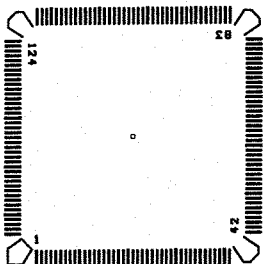
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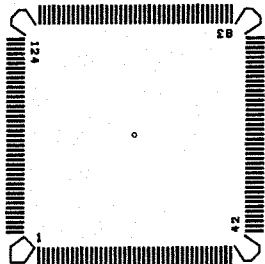
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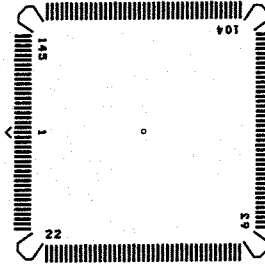
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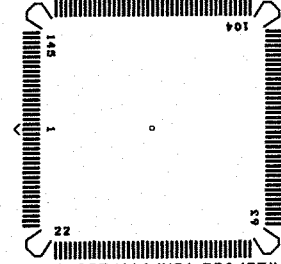
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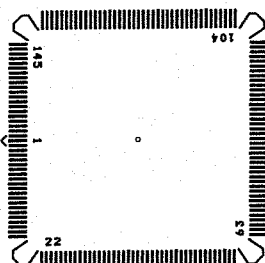
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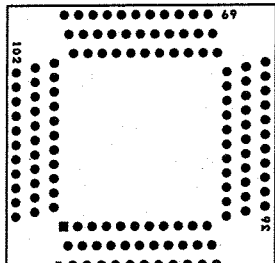
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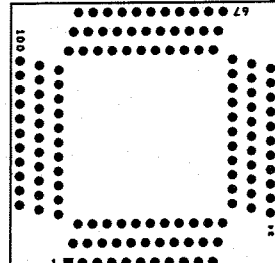
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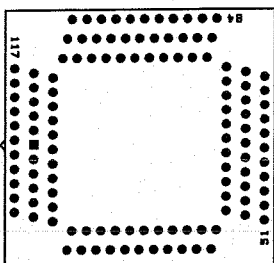
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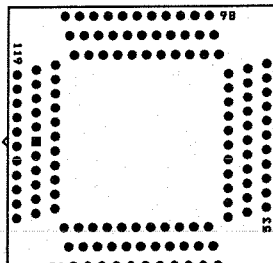
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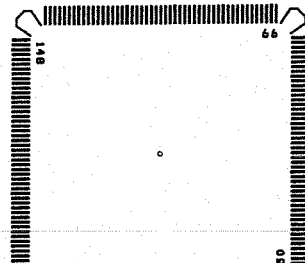
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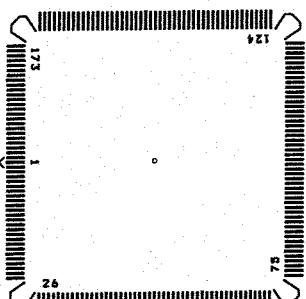
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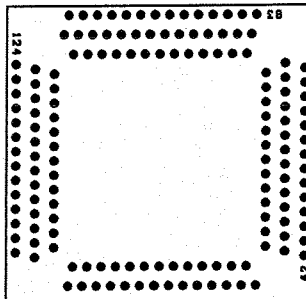
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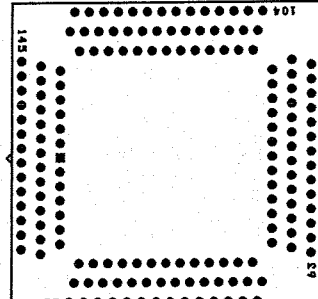
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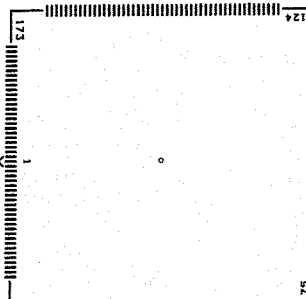
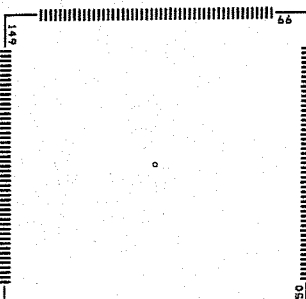
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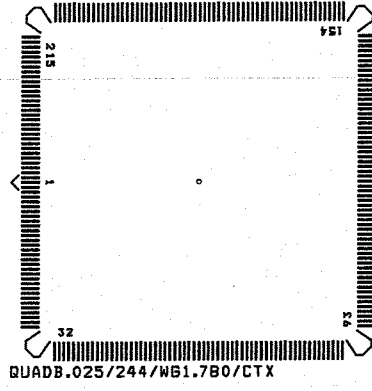
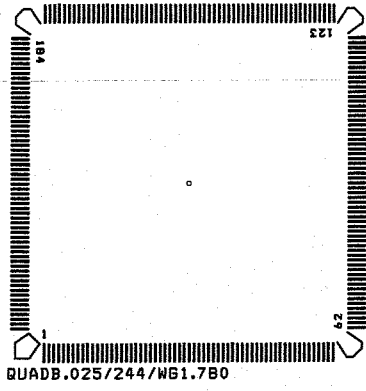
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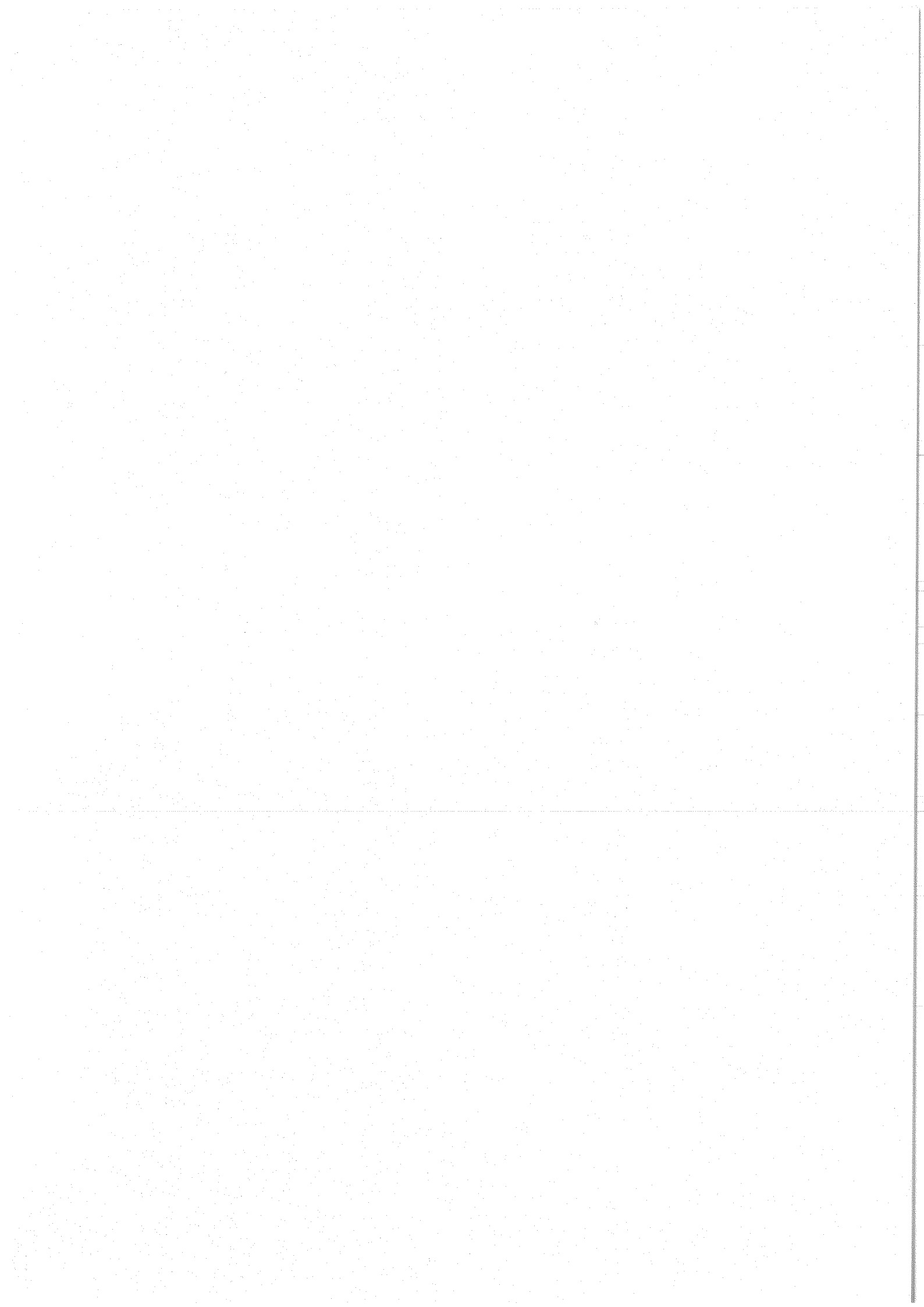


QUADB.025/164/W61.280/TMS2/CTX











RF/SSMB/V



RF/SSMB/R.325



RF/SMA/V



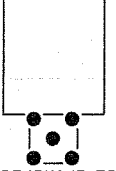
RF/SMB/R.475



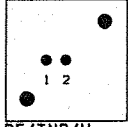
RF/SMA/R.425



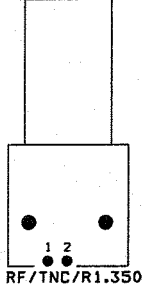
RF/SMB/V



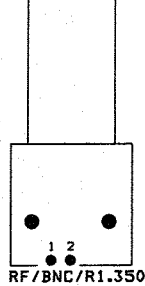
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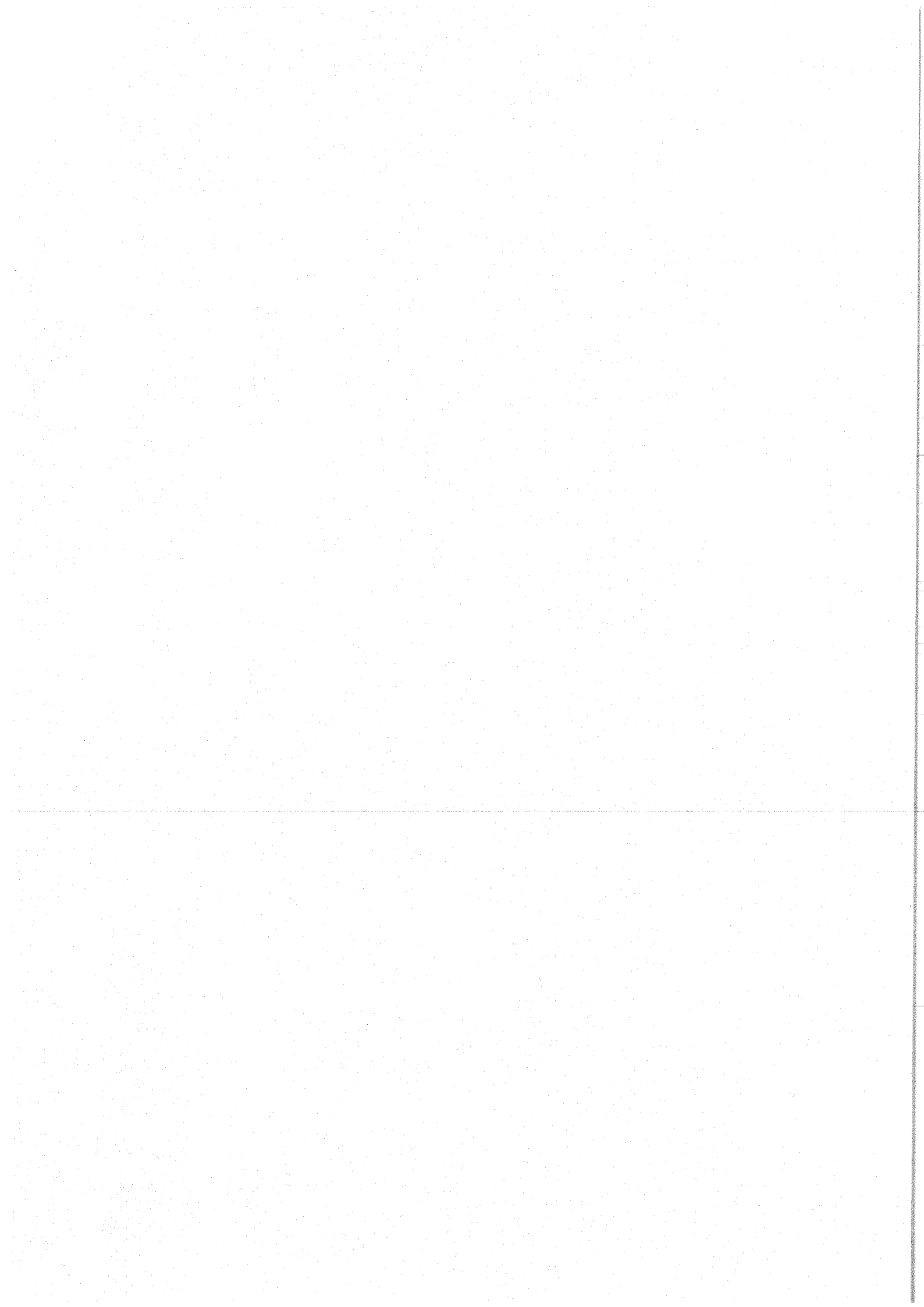
RF/TNC/V

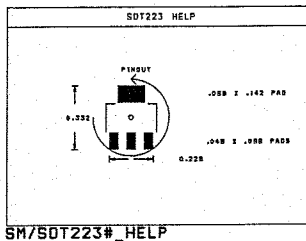
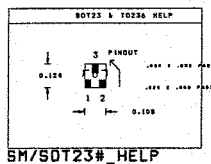
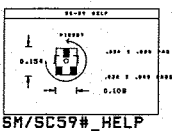
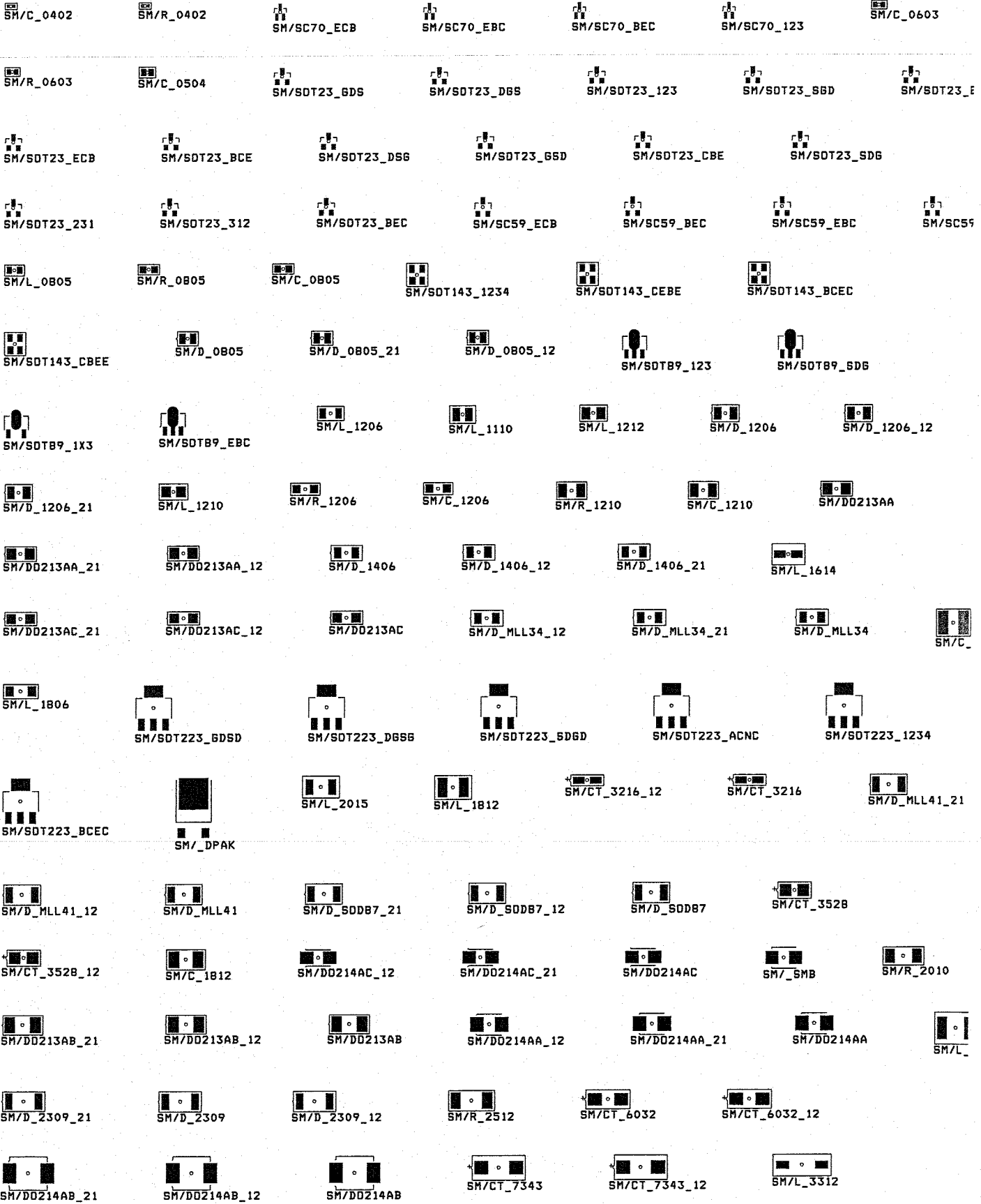


RF/TNC/R1.350



RF/BNC/R1.350






SM DIDDIES

PINOUT : C Cathode, A Anode (DEFAULT)  
: 1 Cathode, 2 Anode  
: 2 Cathode, 1 Anode

Cathode indicators

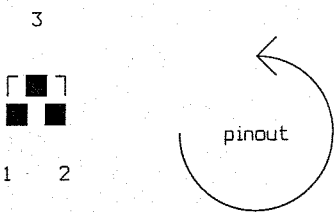


C	A (default)
1	2
2	1

SM/D#\_HELP

SOT CHIPS

PINOUT : Counterclockwise typical all patterns



SM/SOT#\_HELP