

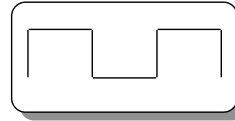
**Chapter 4\_1**  
**Signal Integrity**  
**in Digital Design**

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## The Signals

**FORMAL : Digital**

1 0 1



**REAL : Analog**

2.3, 0.5, 2.3



Signals use a physical magnitude (electrical: voltage, current, charge...) as a vehicle. Consequently, they belong to analog world.

## Noise

⇒ **DC**

- **Supply Voltage**
- **Temperature**

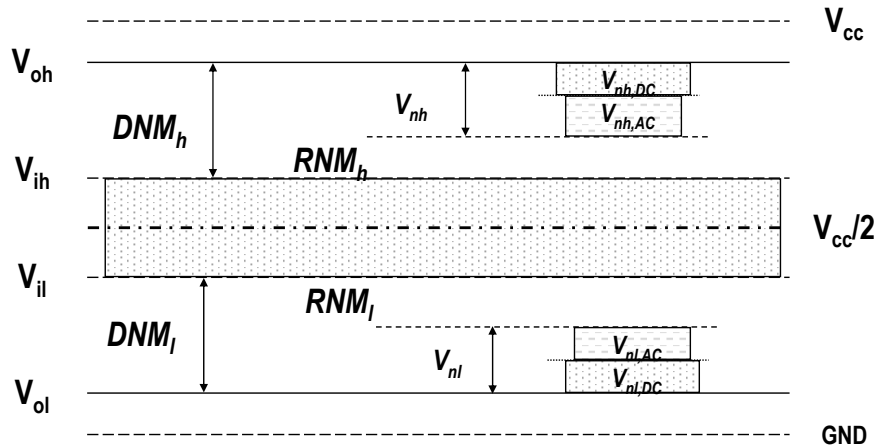
⇒ **AC**

- **Reflexion**
- **Crosstalk**
- **Switching**

Of the energy consumed from power supply, the part that doesn't go to the signal goes to the noise. Consequently, in any conversion process with less than 100% efficiency, noise is generated.

Apart from that, any noise generated outside our system can be coupled to it, be through EM radiation or through conduction.

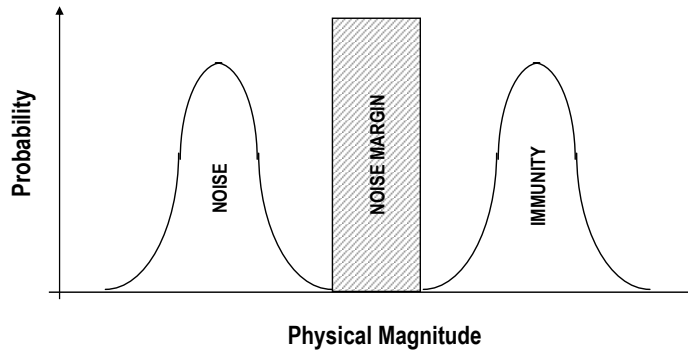
# Noise Margins



They define the tolerance of our system to noise.

## Real Situation

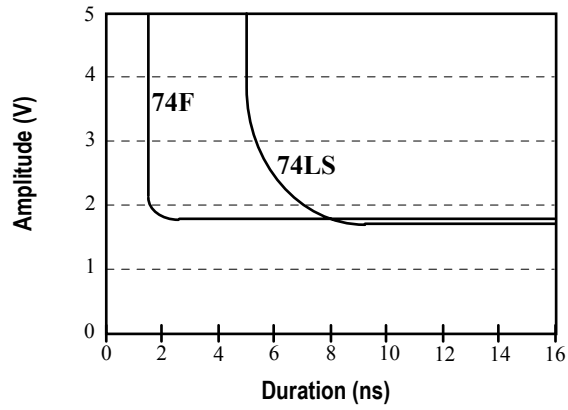
**NOISE and IMMUNITY have Gaussian Distributions**  
**Failure Rate NEVER goes to zero**



Because the physical magnitude has a statistical distribution (supposedly Gaussian) error probability **never goes to zero**. However, it will decrease with increasing the difference between average values and also with decreasing standard deviation.

# AC Noise Immunity

Logic circuits act as LOW PASS Filters



White noise has a constant and infinite spectrum. Bandwidth of our circuit then has to do with the effect of noise. Any noise falling outside the bandwidth will be attenuated in consequence

## DC Noise: Supply Voltage

$$V_X(V) = V_X(V_0) + K_V(V - V_0)$$

$K_V$	
Bipolar	$V_{oh}$ → 1 V/V
	$V_{ol}$ → 0 V/V
MOS	$V_{oh}$ → 1 V/V
	$V_{ol}$ → 0 V/V

Estimated  $K_V$  values according to technology.

## DC Noise: Temperature

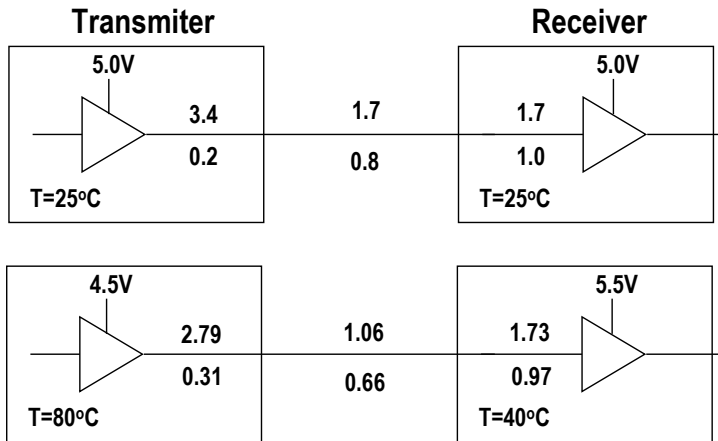
$$V_X(T) = V_X(T_0) + K_T(T - T_0)$$

<b><math>K_T</math></b>	<b>Bipolar</b>	$V_{oh}$ →	de 2 a 4 mv/°C
		$V_{ol}$ →	1 mv/°C
	<b>MOS</b>	$V_{oh}$ →	0.5 mv/°C
		$V_{ol}$ →	0.5 mv/°C

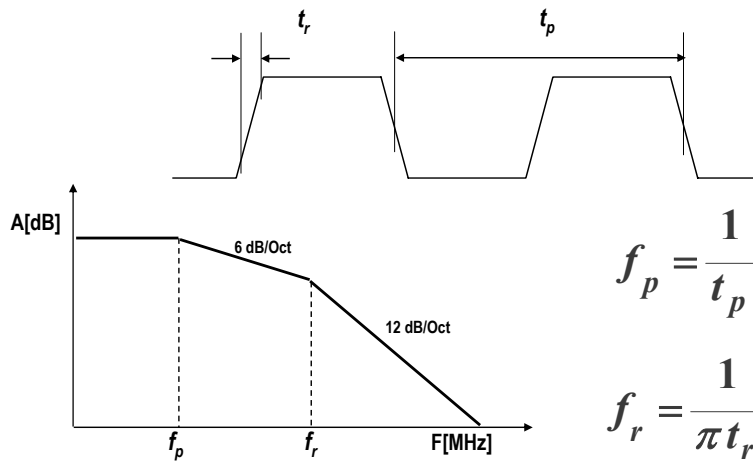
Estimated  $K_T$  values according to technology.



# Example

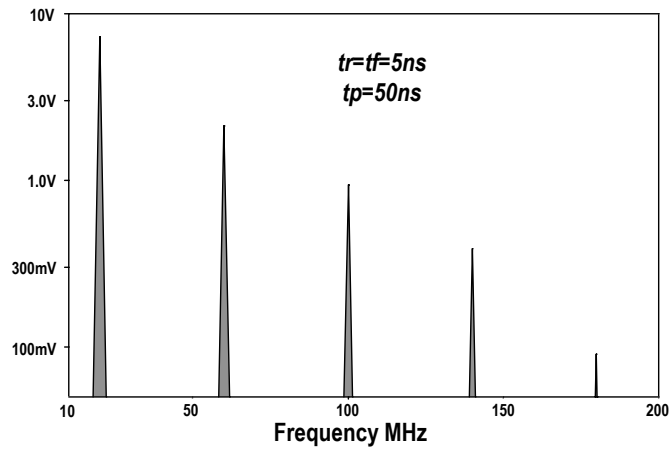


# Spectrum of a Digital Signal

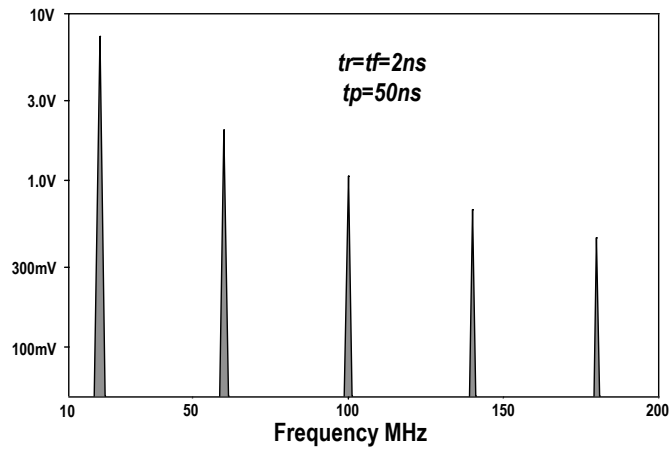


In a digital signal (quasi-periodic and trapezoidal) the two parameters that define the spectrum are:  
Period and transition times (raise and fall)

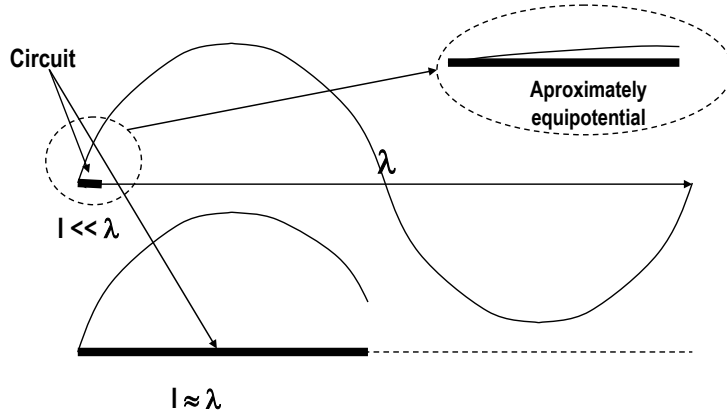
# Spectrum (1)



## Spectrum (2)

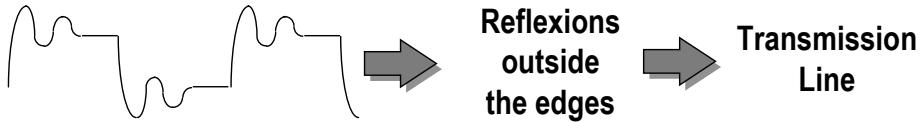


## Wire or Transmission Line? (1)

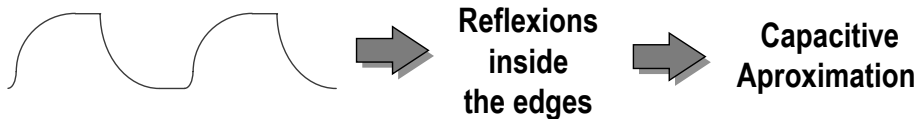


## Wire or Transmission Line? (2)

**Raise Time  $< 2 \times$  Propagation Time**



**Raise Time  $\ll 2 \times$  Propagation Time**



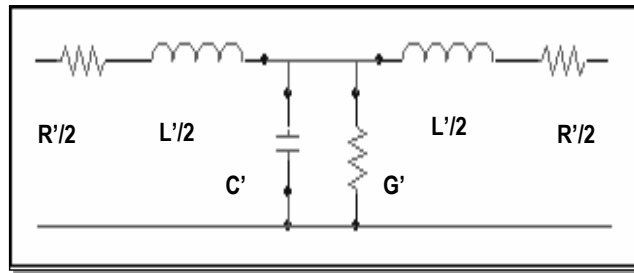
All the electrical magnitudes propagating through our system have a finite velocity ( ideally that of light, if the medium is vacuum or air). Consequently, propagation delay depends on length of the conductor.

If that particular delay is much smaller than the wavelength of the maximum frequency propagating through our system, then it can be considered “equipotential” and propagation velocity can be considered infinite.

On the other side, if this condition is not met, we’ll have to consider our system as distributed and conductors as transmission lines

Therefore, this fact depends on frequency and system “size”.

## Transmission Line Diagram



Line Impedance	$\vec{Z}_0 = \sqrt{\frac{j\omega L' + R'}{j\omega C' + G'}}$
Propagation Factor	$\gamma = \sqrt{(j\omega L' + R')(j\omega C' + G')}$

Associated values to a (distributed) transmission line are

$L'$  Inductance per unit length. Associated to circulating current and its ability to induce magnetic field.

$R'$  Resistance per unit length. Associated to circulating current and its associated voltage drop

$C'$  Capacitance per unit length. Associated to voltage distribution and its ability to produce electric field.

$G'$  Conductance per unit length. Is Associated to dielectric conduction current (losses)

Line Impedance ( instantaneous voltage to current ratio) is Complex and Frequency dependent

Propagation factor is Complex and Frequency dependent

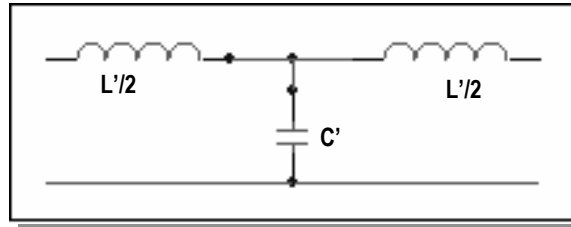
## Frequency ranges in a Transmission Line

- ⇒ Low frequency:  $j\omega L' \approx R'$  and/or  $j\omega C' \approx G'$   
 $Z_0$  and  $\gamma$  complex and function of  $\omega$  : Dispersive regime
- ⇒ Medium frequency:  $j\omega L' \gg R'$  and  $j\omega C' \gg G'$   
 $Z_0$  and  $\gamma$  real and independent of  $\omega$  : Ideal regime
- ⇒ High Frequency:  $j\omega L' > R'$  and/or  $j\omega C' > G'$   
 $Z_0$  and  $\gamma$  complex and function of  $\omega$  : Cutoff regime



## Lossless Transmission Line

In printed circuits and in other connexion means used in digital systems, LOSSES CAN BE NEGLECTED



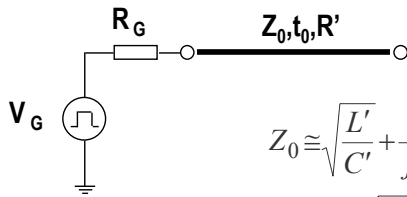
Impedance

$$Z_0 = \sqrt{\frac{L'}{C'}}$$

Propagation time

$$t_0 = \sqrt{L' \cdot C'}$$

## Conductor Losses



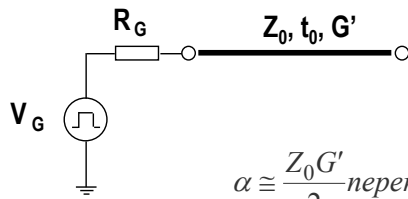
$$Z_0 \cong \sqrt{\frac{L'}{C'}} + \frac{R'}{j2\omega t_0}$$

$$t_0 \cong \sqrt{L'C'}$$

$$\alpha \cong \frac{R'}{2Z_0} \text{ neper / m}$$

$$\alpha \cong 4.343 \frac{R'}{Z_0} \text{ dB / m}$$

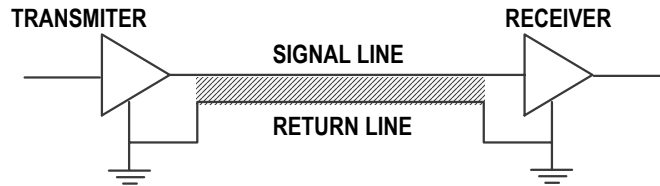
## Dielectric Losses



$$\alpha \cong \frac{Z_0 G'}{2} \text{ neper / m}$$

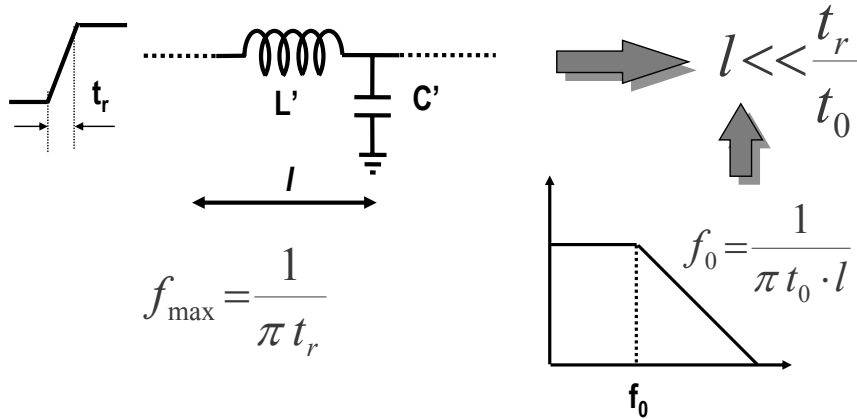
$$\alpha \cong 4.343 \cdot Z_0 G' \text{ dB / m}$$

## Transmission Line Elements



- ➡ Signal line conducts the signal current
- ➡ Return line conducts a return current equal to signal current
- ➡ Cross-hatched area determines the ability to radiate or sensitivity to radiation.

## Transmission Line Simulation

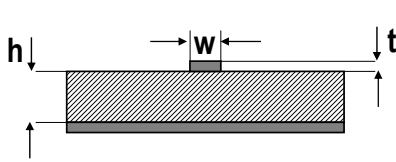


To simulate a transmission line (i.e., with PSPICE) we have to discretize it by substituting a given number of L-C sections. This sections are in fact low pass filters so we have to make sure that their bandwidth is enough to leave response substantially unaltered.

## Examples of Transmission Lines

Line Type	$L'$ (nH/cm)	$C'$ (pF/cm)	$Z_0$ ( $\Omega$ )	$t_d$ (ns/m)
Free space	$\mu_0$	$\epsilon_0$	370	3,3
Wire on ground	20	0,06	600	~4
Twisted pair	5-10	0,5-1	80-120	5
Flat cable	5-10	0,5-1	80-120	5
PC track	5-10	0,5-1,5	70-100	~5
Coaxial cable	2,5	1	50	5

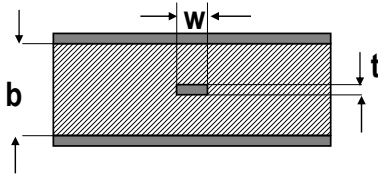
## Transmission Lines in PCB



### Microstrip

$$Z_0 = \frac{87}{\sqrt{\epsilon_r + 1.41}} \ln\left(\frac{5.98h}{0.8w + t}\right) \Omega$$

$$t_0 = 3.34 \sqrt{0.475\epsilon_r + 0.67} \text{ ns / m}$$



### Strip Line

$$Z_0 = \frac{60}{\sqrt{\epsilon_r}} \ln\left(\frac{4b}{0.67\pi(0.8w + t)}\right) \Omega$$

$$t_0 = 3.34 \sqrt{\epsilon_r} \text{ ns / m}$$

## Example

### Microstrip

$$Z_0 = 69.4 \Omega$$
$$t_0 = 5.8 \text{ ns/m}$$

### DATA

$$\epsilon_r = 4.6$$
$$w = 0.010'' (0.254 \text{ mm})$$
$$t = 0.0014'' (0.036 \text{ mm})$$
$$b = 0.020'' (0.5 \text{ mm})$$



### DATA

$$\epsilon_r = 4.7$$
$$w = 0.010'' (0.254 \text{ mm})$$
$$t = 0.002'' (0.05 \text{ mm})$$
$$h = 0.012'' (0.3 \text{ mm})$$

### Strip Line

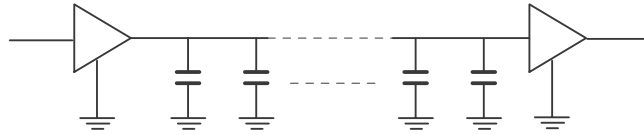
$$Z_0 = 50.7 \Omega$$
$$t_0 = 7.3 \text{ ns/m}$$





## Real situation: The loaded line

Elements connected to the line act generally as a capacitive load.  
In a first approximation, we can treat them as distributed.



**LOAD FACTOR**

$$\Rightarrow f_L = \sqrt{1 + C'_L / C'}$$

**Impedance decreases**

$$\Rightarrow Z_L = Z_0 / f_L$$

**Propagation time increases**

$$\Rightarrow t_L = t_0 \cdot f_L$$

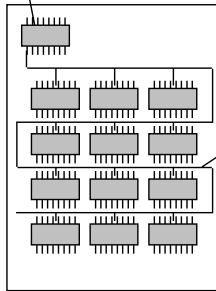
Loaded line has:  $L'$  and  $(C' + C'_L)$

where  $C'_L = C_L / d$

where  $d$  is the distance between loads

## Example 1: PCB track

CLK GENERATOR



TOTAL CLK TRACK  
LENGTH = 500mm

**DATA:**

$$Z_0 = 100 \Omega$$

$$C' = 1 \text{ pF/cm}$$

$$C_i = 15 \text{ pF}$$

$$\tau_i = 5 \text{ ns/m}$$

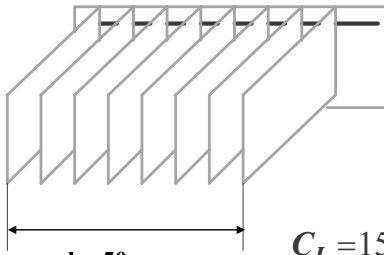
$$C_L = 15 \text{ pF} \cdot 12 / 50 \text{ cm} = 3.6 \text{ pF/cm}$$

$$f_L = \sqrt{1 + 3.6/1} = 2.145$$

$$Z_L = 100 / 2.145 = 46.6 \Omega$$

$$t_L = 5 \cdot 2.145 = 10.725 \text{ ns/m}$$

## Example 2: Bus line



$N = 20$

**DATA:**

$$Z_0 = 100 \Omega$$

$$C' = 0.6 \text{ pF/cm}$$

$$C_i = 15 \text{ pF}$$

$$\tau_i = 6 \text{ ns/m}$$

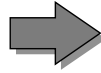
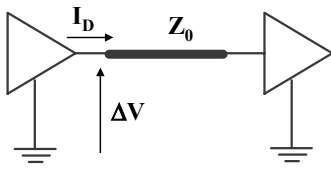
$$C_L = 15 \text{ pF} \cdot 20 / 50 \text{ cm} = 6 \text{ pF/cm}$$

$$f_L = \sqrt{1 + 6 / 0.6} = 3.317$$

$$Z_L = 100 / 3.317 = 30 \Omega$$

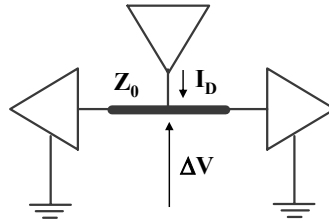
$$t_L = 6 \cdot 3.317 = 19.9 \text{ ns/m}$$

## Driver's problem(1)



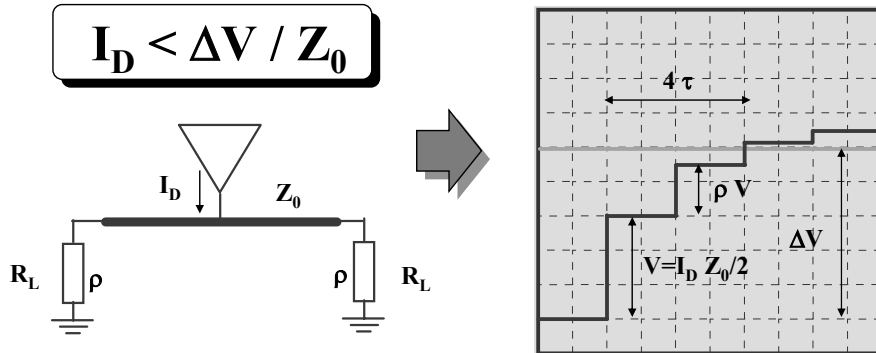
$$\Delta V = I_D \cdot Z_0$$

$$\Delta V = I_D \cdot \frac{Z_0}{2}$$



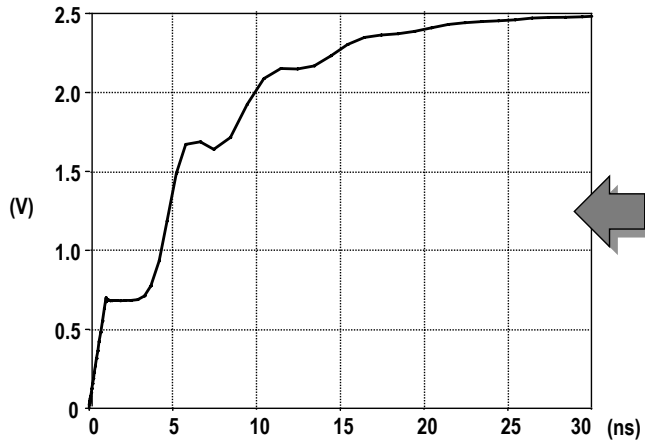
## Driver's problem (2)

If we NEED  $\Delta V$  and we HAVE  $I_D$  and  $Z_0$ :



If the voltage change is not large enough to produce a state change, then the change cannot occur at leading edge (Incident switching) and we have to rely in eventual positive reflexions.

## Driver's problem simulation



**DATA:**

$l = 10 \text{ cm}$

$Z_0 = 100 \Omega$

$t_0 = 6 \text{ ns/m}$

$t_r = 1 \text{ ns}$

$C_L = 6 \text{ pF/cm}$

$I_D = 50 \text{ mA}$

