PROGRAM: Master of Science in Information and Communication Technologies
COURSE: VLSI Digital Design

	Technical Description	Activity with the lecturer			Student Activity			Assessment		ECTS Hours	ECTS
		Objectives	Weekly Hours	Total Hours	Student Work	Weekly Study/ Work Hours	Total Study/ Work Hours	Procedure	Time (hours)		
Theory	Lectures and application sessions	Digital VLSI design Fundamentals	2 (x 5) 2 (x 3)	10 6	Personal study	1	15	Mid-term exam Final exam	1 2	34	1.24
Problems	Guided problem solving	Application of knowledge. Critical thinking development	1 (x 4)	4	Problem solving	1	15	Problem resolution delivery	-	19	0.69
Lab	Lab assignments	Digital design with HDLs	2 (x 13)	26	Lab preparation and reports	2 (x 13)	26	Report assessment Lab exam	- 1	53	1.93
Directed activities	Design project	Practical VLSI design (eg. IP core)	1 (x 4)	4	Project realization	1.53	25.5	Project presentation	2	31.5	1.15
Other activities	-	-	-	-	-	-	-	-	-	-	-
Total				50			81.5		6	137.5	5